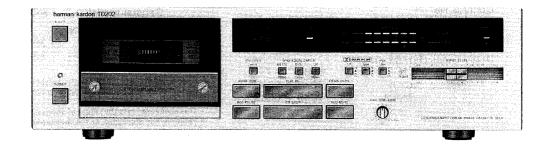
The Harman Kardon Model TD102/TD202

Manual 108A

ULTRAWIDEBAND LINEAR PHASE CASSETTE DECK

Technical Manual



SPECIFICATIONS

OI EOII IOATIOI	•			
,		Nominal Limit		
Track Configuration		4-track 2 Channel Stereo		
.,	-	Cassette Deck		
Record/Playback Tap	e Speed			
Deviation 4.75		$0.05\% \pm 1.5\%$		
Drift 4.75 cm/s	sec.	$0.2\% \pm 2.0\%$		
Wow and Flutter		0.05%(NAB)≤0.1%		
		$0.2\%(CCIR) \le 0.3\%$		
Take Up Torque		50gr. cm 35~70gr. cm		
Back Tension		4gr. cm 2 ~ 6gr. cm		
F. FWD Torque		100gr.cm 70~150gr.cm		
REW Torque		100gr.cm 70~150gr.cm		
F. FWD/REW Time		90 sec. ≤ 100 sec.		
(C-60 Tape)				
Motor		Direct Drive motor		
Bias Frequency		$105kHz \pm 5kHz$		
Playback Output		460mV ± 1.5dB		
Signal-to-Noise Ratio				
at Line Input (Input		00mV)		
IHF-A WTD at Doll	y Level			
Dolby NR Off				
	LN	51dB		
	CrO ₂	54dB		
	Metal	54dB		
Dolby B NR				
	LN	61dB		
	CrO ₂ Metal	64dB ≥ 60dB		
	64dB ≥ 60dB			
Dolby C NR (T				
	LN	66dB		
	CrO ₂	70dB ≥ 66dB		
	Metal	70dB ≥ 66dB		

	Nominal Limit	
Channel Separation	45dB ≥ 35dB	
Crosstalk	70dB ≥ 60dB	
Record/Playback Distortion (Input 1kHz)		,
LN	$1.0\% \le 2.0\%$	(
CrO ₂	1.8% ≤3.0%	
Metal	1.0% ≤2.0%	
MPX Filter Attenuation		
at 15kHz	$0.3dB \le 1dB$	
at 19kHz	35dB ≥ 30dB	
Erase Ratio (Input 80Hz)		
LN	70dB ≥ 60dB	
Metal	61dB ≥ 56dB	
Input Sensitivity	52mV 40(min) ~ 100(max) mV	
(Input 1kHz) at Line Input		
Input Impedance (Input 1kHz) at Line Input	23 k Ω 19(min) ~ 30(max) k Ω	
DIMENSIONS (WxHxD)	17-1/2" x 4-7/8" x 10-1/2" (443 x 122 x 264 mm)	
WEIGHT	10lbs. (4.5kg)	
POWER SUPPLY		
U.S.A. model	AC 120V, 60Hz	
General model	AC 220/240V, 50/60Hz	6
POWER CONSUMPTION		•
U.S.A. model	20W	
U.S.A. IIIOGEI	2000	

Specifications and components subject to change without notice. Overall performance will be maintained or improved.

General model

SIMPSON MODEL 229 ETC. FOR

22W

This specification is the target of servicing. But, there is a case that the specification is not applicable to the measurement condition and instrument.

LEAKAGE TEST (FOR SERVICE ENGINEERS IN THE U.S.A.)

Before returning the unit to the user, perform the following safety checks:

- Inspect all lead dress to make certain that leads are not pinched or that hardware is not lodged between the chassis and other metal parts in the unit.
- Be sure that any protective devices such as nonmetallic control knobs, insulating fishpapers, cabinet backs, adjustment and compartment covers or shields, isolation resistor-capacity networks, mechanical insulators, etc. which were removed for servicing are properly reinstalled.
- Be sure that no shock hazard exists; check for leakage current using Simpson Model 229 Leakage Tester, standard equipment item No. 21641, RCA Model WT540A or use alternate method as follows:

Plug the power cord directly into a 120-volt AC receptacle (do not use an Isolation Transformer for this test). Using two clip leads, connect a 1500 Ohm, 10-watt resistor paralleled by a 0.15 μF capacitor, in series with all exposed metal cabinet parts and a known earth ground, such as a water pipe or conduit. Use a VTVM or VOM with 1000 Ohms per volt, or higher, sensitivity to measure the AC voltage drop across the resistor. (See Diagram.) Move the resistor connection to each exposed metal part having a return path to the chassis (antenna, metal, cabinet, screw heads, knobs and control shafts, escutcheon, etc.) and measure the AC voltage drop across the

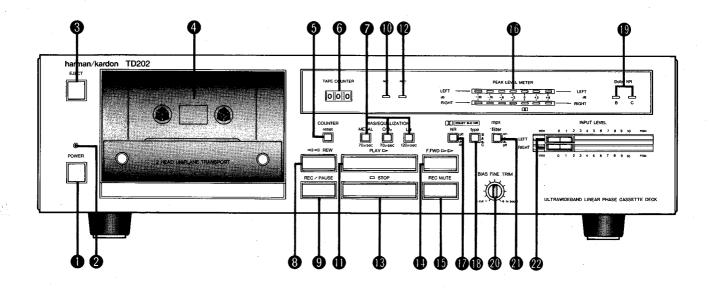
LEAKAGE TEST TO AC GROUND SUCH TO EACH EXPOSED AS WATER PIPE, BX CABLE, METAL SURFACE OF UNIT UNDER TEST HIGH CONDUIT, ETC. GROUND VOLTAGE LEAD OR + LEAD VTVM AC SCALEO $1.5k\Omega$ 10W 0.15µF TEST PROBE TO EXPOSED CONNECT TO KNOWN **METAL PARTS EARTH GROUND**

resistor. (This test should be performed with the power switch in both the On and Off positins.)

A reading of 0.35 volt RMS or more is excessive and indicates a potential shock hazard which must be corrected before

returning the unit to the owner.

CONTROLS AND FUNCTIONS



NOTE: The accompanying illustrations show the TD202.

1 POWER SWITCH (POWER)

Pressing this switch will turn on the power and the POWER indicator will light up. Press the switch again to turn the power off.

2 POWER INDICATOR

© EJECT BUTTON (EJECT)

The soft eject mechanism opens the door slowly when this button is pressed.

CAUTION: This button cannot be depressed while the tape is running. Be sure to press the "STOP" button before pressing the "EJECT" button.

4 CASSETTE COMPARTMENT

G COUNTER RESET BUTTON (COUNTER reset)

1 TAPE COUNTER

For a digital indication of the position on a cassette tape. The figure changes as the tape runs. Cueing for the start of a selection is facilitated by making a note of the counter reading.

TAPE SELECTORS (BIAS/EQUALIZATION)

For selection of the record and playback circuitry that provides the lowest distortion and flattest frequency response for metal, chromium dioxide (CrO₂) or low noise (LN) tape.

@ REWIND BUTTON (REW)

Press this button to rewind a tape at high speed.

9 RECORD/PAUSE BUTTON (REC/PAUSE)

Press this button to provide the record standby mode. The "REC" indicator will illuminate and the "PLAY" indicator will blink. Recording starts when the "PLAY" button is pressed. The "PLAY" indicator will then stop blinking and remain illuminated. Also, press this button to temporarily stop recording.

® RECORD INDICATOR

For indication that the tape is being recorded.

1 PLAY BUTTON (PLAY)

Press this button to start playback.

® PLAY INDICATOR

For indication that the tape is playing.

®STOP BUTTON (STOP)

Press this button to stop each operation. Pressing this button stops the playback, recording, fast forward and rewind modes. It also cancels the record standby mode activated by the "REC/PAUSE" button.

FAST FORWARD BUTTON (F.FWD)

Press this button to quickly advance the tape in the same direction as it is played.

GRECORD MUTE BUTTON (REC MUTE)

This button allows you to create a silent segment of tape at any time while recording. The button is a momentary contact type and will not lock in the depressed position. The record mute feature will only operate while the button is held in the depressed position.

® PEAK LEVEL METER

The level of the signal being recorded or played is displayed clearly on this meter.

TODOLBY* NR SWITCH (NR)

Depress this switch for recording or playback using the Dolby NR system. The "Dolby NR" indicator will light up. Press the switch again to turn off the Dolby NR system.

® DOLBY NR TYPE SWITCH (type) (TD202 only)

For selection of the Dolby B- or C-type NR system. Depress this switch to select the Dolby C-type NR system. Press it again to select the Dolby B-type system. The green "Dolby NR" indicator (for B-type) or the amber one (for C-type) illuminates according to the "type" switch position.

® DOLBY NR INDICATOR

For indication that Dolby noise reduction circuitry is activated.

® BIAS FINE TRIM KNOB (BIAS FINE TRIM) (TD202 only) For precise adjustment of the bias used during recording.

MPX FILTER SWITCH (mpx filter)

The MPX filter is a high frequency filter that has very little effect below 16kHz, but has 30dB attenuation at 19kHz, the frequency of the FM stereo pilot signal. Set this switch to the "on" position when recording from an FM stereo tuner or receiver. However, to appreciate the ultrawideband frequency response of your cassette deck, depress this switch to the "off" position when recording all other sources, such as a turntable, tape deck, etc.

@INPUT LEVEL CONTROL KNOBS (INPUT LEVEL)

These knobs adjust the record level of the input signal. The upper side knob is for the left channel and lower one is for the right channel.

^{*}Dolby noise reduction manufactured under license from Dolby Laboratories Licensing Corporation. "Dolby" and the double-D symbol are trademarks of Dolby Laboratories Licensing Corporation.

DISASSEMBLY PROCEDURES (REFER TO PAGES 15, 23 AND 29)

1 CABINET TOP (126) REMOVAL

Remove 6 screws @ and then remove the Cabinet Top (126).

2 FRONT PANEL ASSEMBLY (101) REMOVAL

- 1. Remove the Cabinet Top (126), referring to the previous step 1.)
- 2. Pull out the Shaft (194) with the Push Button (147).
- 3. Remove the Belt (196) of the Tape Counter.
- Disconnect the connectors (LCN201 and LCN202) connected to the Cassette Tape Recorder Mechanism Assembly (107).
- Disconnect the connectors (CN1 and CN2) connected to the Main P. C. Board (PCB-1).
- Remove 6 screws and then remove the Front Panel Assembly (101) with the Cassette Tape Recorder Mechanism Assembly (107).
- 7. Remove 4 screws (a) and then remove the Cassette Tape Recorder Mechanism Assembly (107).

3 MAIN P. C. BOARD (PCB-1) REMOVAL

- Remove the Cabinet Top (126), referring to the previous step 1.
- Disconnect the connectors (LCN201 and LCN202) connected to the Cassette Tape Recorder Mechanism Assmbly (107).
- 3. Disconnect the connectors (CN1 and CN2) connected to the Main P. C. Board (PCB-1).
- Open the lid of connectors (CN101 ~CN105, CN107, CN108 and CN110) on the Main P. C. Board (PCB-1) and then disconnect the lead wires.

- Open the lid of connector (CN109) on the Power Supply
 P. C. Board (PCB-3) and then disconnect the lead wires.
- 6. Remove 3 screws
 and then remove the Main P. C. Board (PCB-1).

4 DISPLAY P. C. BOARD (PCB-2) REMOVAL

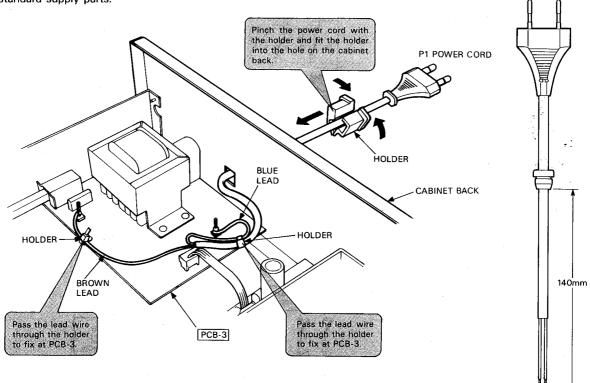
- 1. Remove the Cabinet Top (126), referring to the previous step 1.)
- Open the lid of connectors (CN101, CN102 and CN105) on the Main P. C. Board (PCB-1) and then disconnect the lead wires.
- Open the lid of connectors (CN106) on the Function Switches P. C. Board (PCB-5) and then disconnect the lead wires.
- Remove 3 screws
 and then remove the Display P. C. Board (PCB-2).

5 FUNCTION SWITCHES P. C. BOARD (PCB-5) REMOVAL

- 1. Remove the Front Panel Assembly (101), referring to the previous step 2.)
- 2. Pull out the Push Button Assembly (105).
- Open the lid of connectors (CN103 and CN104) on the Main
 P. C. Board (PCB-1) and then disconnect the lead wires.
- Open the lid of connector (CN106) on the Function Switches P. C. Board (PCB-5) and then disconnect the lead wires.
- Remove 2 screws and then remove the Function Switches P. C. Board (PCB-5).

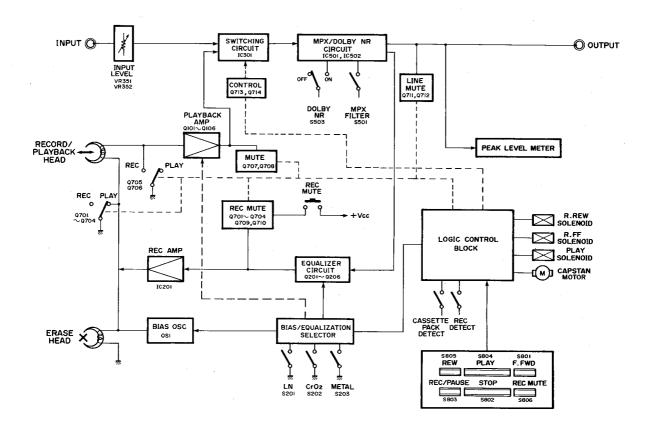
POWER CORD REPLACEMENT (FOR SERVICE ENGINEERS OTHER THAN NORTH AMERICA)

In order to prevent fire or shock hazard when replacing the power cord, follow the procedure below to replace the part with the standard supply parts.

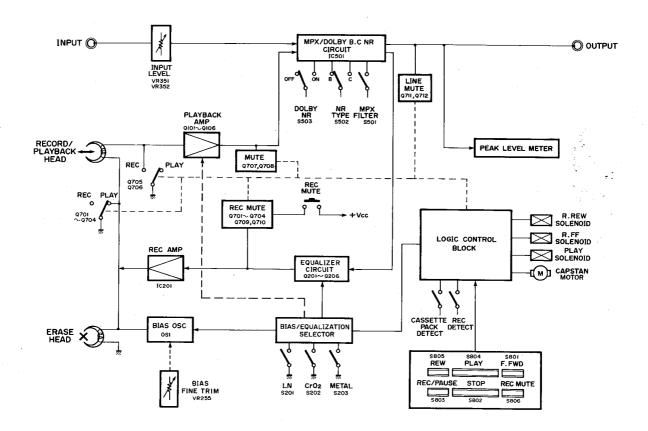


BLOCK DIAGRAM

TD102



TD202



CIRCUIT DESCRIPTION

PLAYBACK SIGNAL (MODEL TD102)

The signal from the playback head is amplified by the playback amplifiers Q101, Q103, Q105 (L ch.) and Q102, Q104, Q106 (R ch.), and is applied to the pins (B) (L ch.) and (4) (R ch.) of the switching IC301. Then it is output from the pins (9) (L ch.) and (3) (R ch.) of the same IC301 and is applied to the pin (4) of the Dolby NR IC501 (L ch.) and IC502 (R ch.) (B type).

IC301 is usually switched to the playback mode. However, the control signal transmitted from IC801 through Q326 and Q325 switches IC301 from the record mode to the playback mode. Also, this control signal turns ON between the pins (a) and (a) on the left channel and between pins (a) and (4) on the right channel respectively.

The input signal to the Dolby NR ICs is out from the pin ® of IC501 (L ch.) and IC502 (R ch.), and is applied to the PEAK LEVEL METER circuit consisting of the OUTPUT jack, IC401 (L ch.) and IC402 (R ch.).

The characteristics of the playback equalizer are defined by the BIAS/EQUALIZATION switch and are selected and specified in Q107 (L ch.) and Q108 (R ch.).

RECORD SIGNAL (MODEL TD102)

The signal from the INPUT jack is controlled by the INPUT LEVEL control and is applied to the pins 1 (L ch.) and 1 (R ch.) of the switching IC301. Then it is output from the pins 1 (L ch.) and 2 (R ch.) of IC301 and applied to the pin 4 of the Dolby NR IC501 (L ch.) and IC502 (R ch.) (B type). The control signal transmitted from IC801 through Q326 and Q325 switches IC301 from the playback mode to the record mode. Also, this control signal turns ON between pins 1 and 2 on the left channel and between the pins 1 and 2 on the right channel of IC301.

The input signal to the Dolby NR ICs is output from the pin (§) of the IC501 (L ch.) and IC502 (R ch.) and passes through the MPX filter. Then it is input to the pin (§) and is output from the pin (§) of IC501 (L ch.) and IC502 (R ch.). The output signal from the Dolby NR ICs passes through the record equalizer circuit and is amplified by the record amplifier. The amplified signal is then applied to the recording head after being synthesized by a bias signal.

PLAYBACK SIGNAL (MODEL TD202)

The signal from the playback head is amplified by the playback amplifiers Q101, Q103, Q105 (L ch.), and Q102, Q104, Q106 (R ch.), and is applied to the pins ③ (L ch.) and ④ (R ch.) of the Dolby NR IC501 (B/C type). Switching of the playback signal from the record mode (external input signal) to the playback mode is performed inside IC501.

IC501 is usually switched to the playback mode. However, the control signal transmitted to the pin 3 of IC501 from IC801 through Q507 and Q508 switches IC501 from the record mode to the playback mode. The input signal to IC501 is output from the pins 9 (L ch.) and 4 (R ch.) and applied to the OUTPUT jack and the PEAK LEVEL METER circuit. The characteristics of the playback equalizer are defined by the BIAS/EQUALIZATION switch and are selected and specified in Q107 (L ch.) and Q108 (R ch.).

RECORD SIGNAL (MODEL TD202)

The signal from the INPUT jack is controlled by the INPUT LEVEL control and is applied to pins ③ (L ch.) and ③ (R ch.) of the Dolby NR IC501 (B/C type). Switching of the record signal from the playback mode to the record mode is performed inside IC501.

The control signal transmitted to the pin (38) of IC501 from IC801 through Q507 and Q508 switches IC501 from the playback mode to the record mode.

The input signal to the Dolby NR IC is output from pins 6 (L ch.) and 3 (R ch.) of IC501 and passes through the MPX filter. Then it is input to the pins 7 (L ch.) and 3 (R ch.) and is output from the pins 1 (L ch.) and 2 (R ch.).

The signal output from IC501 passes through the record equalizer circuit and is amplified by the record amplifier of IC201. The amplified signal is then applied to the recording head after being synthesized by a bias signal.

LOGIC IN RECORD MODE (TD102)

When the "REC" button is pressed, the pin 4 of IC801 becomes high level and Q705 (L ch.) and Q706 (R ch.) turn ON. The input to the playback amplifiers is muted. Also Q803 and Q805 turn ON and Q808 turns OFF. Therefore Q701, Q703 (L ch.) and Q702, Q704 (R ch.) turn OFF to release the muting of the outputs from the record amplifiers.

Also Q505 turns ON to make the pin ⑦ of IC501 (L ch.) and IC502 (R ch.) low level. Therefore the mode is switched to the record mode

SWITCHING FROM RECORD MODE TO PLAYBACK MODE IN LOGIC (TD102)

When the "STOP", "PAUSE" or "PLAY" button is pressed, the pin ② of IC801 becomes low level. Q705 (L ch.) and Q706 (R ch.) turn OFF to release the muting of the inputs to the playback amplifiers. Also, Q803 and Q805 turn OFF and Q808 turns ON to turn ON Q701, Q703 (L ch.) and Q704 (R ch.). Therefore the outputs from the record amplifiers are muted.

Also Q505 turns OFF to make the pin ⑦ of IC501 (L ch.) and IC502 (R ch.) high level. Therefore the mode is switched to the playback mode.

LOGIC IN RECORD MODE (TD202)

When the "REC" button is pressed, the pin 4 of IC801 becomes high level and Q705 (L ch.) and Q706 (R ch.) turn ON. The input to the playback amplifiers is muted. Also Q803 and Q805 turn ON and Q808 turns OFF. Therefore Q701, Q703 (L ch.) and Q702, Q704 (R ch.) turn OFF to release the muting of the outputs from the record amplifiers. Also, Q507 turns ON and Q508 turns OFF to make the pin 3 of IC501 high level. Therefore the mode is switched to the record mode.

SWITCHING FROM RECORD MODE TO PLAYBACK MODE IN LOGIC (TD202)

When the "STOP", "PAUSE" or "PLAY" button is pressed, the pin ② of IC801 becomes low level. Q705 (L ch.) and Q706 (R ch.) turn OFF to release the muting of the inputs to the playback amplifiers. Also, Q803 and Q805 turn OFF and Q808 turns ON to turn ON Q701, Q703 (L ch.) and Q702, Q704 (R ch.). Therefore the outputs from the record amplifiers are muted.

Also, Q507 turns OFF and Q508 turns ON to make the pin ® of IC501 low level. Therefore the mode is switched to the playback mode.

MUTING (MODELS TD102 AND TD202)

The signal that mutes the sound produced at switching to recording or playback is applied from IC801 of the logic control block.

When the "STOP" button is pressed, the mute signal output from the pin 6 of IC801 turns ON Q707 (L ch.) and Q708 (R ch.) to short-circuit the output signals of the playback amplifiers for muting. Also, this mute signal turns ON Q713 and Q714 as well as Q711 (L ch.) and Q712 (R ch.) to mute the output line signal from the Dolby NR ICs.

For the purpose of preventing generation of noise at power ON/OFF, the mute signal is output from Q51. The muting is done by short circuiting the output signal with Q711 (L ch.) and Q712 (R ch.) turned ON.

ALIGNMENT PROCEDURES (REFER TO PAGES 24, 25, 27 AND 28 FOR TD102 OR 30, 31, 33 AND 34 FOR TD202)

■ CASSETTE MECHANISM CONFIRMATION

Make sure to confirm conditions of the cassette mechanism as follows before adjustment.

1. Confirmation of erroneous erase preventive function

- The switch should turn ON when a tape with erroneous erase preventive pawl is inserted. (Use a tape which is 0.2mm smaller than the minimum size of 62.9mm or a MAZ-0184-C gauge one.)
- When the switch arm is moved back gradually from the ON position, the switch should turn OFF.

2. Confirmation of cassette pack detection function

- The switch should turn ON when a tape is inserted. (Use a tape whose minimum size is 63.5mm or a MAZ-0184-C gauge one.)
- When the switch arm is moved back gradually from the ON position, the switch should turn OFF.

3. Confirmation of eject function

- The cassette compartment opens smoothly and no abnormal noise should be heard while opening and closing.
- The eject lock arm opens smoothly without contacting the chassis and damper.
- The eject button can not be pressed during playback.

4. Confirmation of playback, fast forward and rewind functions

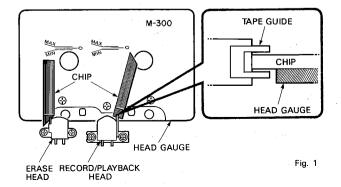
 The torque used in each of the playback, fast forward and rewind modes should be within specification.

 No abnormal noise should be heard during operation in any mode. The solenoid switching sound should not be considered as a noise.

Confirmation of positions of record/playback head and erase head

Head height

- a) Set the M-300 head gauge.
- b) Set the unit in the playback mode and place the adjustment chip on the head gauge as shown in the Fig. 1.
- c) The adjustment chip should not contact the tape guide of both record/playback head and erase head.



Head position

- a) Set the M-300 head gauge.
- b) Set the unit in the playback mode and place the adjustment chip on the head gauge as shown in the Fig. 2.
- with both record/playback head and erase head, the adjustment chip should be between MIN and MAX of the M-300 head gauge.

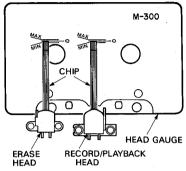


Fig. 2

■ ELECTRICAL ADJUSTMENT AND CONFIRMATION

1. Before adjustment

- Before electriacl adjustment, make sure that confirmations of the cassette mechanism are all completed.
- After the power switch is pushed on, wait for 10 minutes before measuring to be sure of the most stable operation.
- Since head magnetization, dust accumulations, etc. are likely to introduce errors in the various characteristics, it is very important that the heads are properly demagnetized and cleaned before commencing any adjustment, particularly frequency response and head azimuth adjusment.

2. Instruments required

- Low frequency oscillator
- AC VTVM or dual channel AC VTVM
- Oscilloscope
- Wow/flutter meter
- Frequency counter

3. Test tapes

 Azimuth adjustment 	MTT-114
Tape speed adjustment	. MTT-111 or MTT-111D

Playback output level adjustment

Playback frequency characteristic confirmation
 TCC-162C, TCC-262C

Reference tapes

LN	SCC-502
CrO ₂	SCC-504
METAL	SCC-565

Note:

C-90 differes with C-60 in the thickness and bias is of unequal, so adjust with the tape whose bias in of specified value.

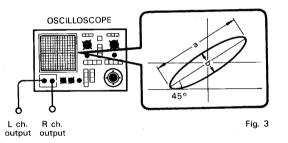
4. General conditions (unless otherwise noted)

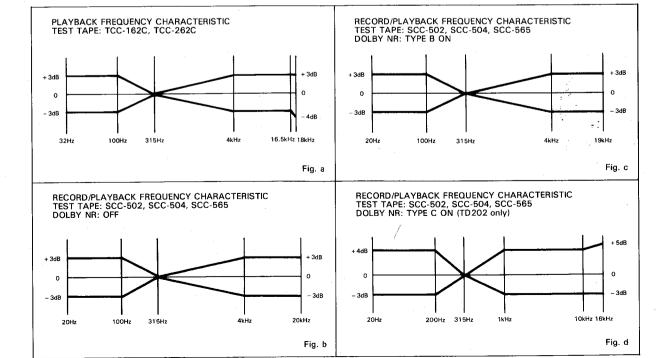
Controls and Switches	Settings
Dolby NR	Off
Input Level	Maximum
MPX Filter	Off
Bias Fine Trim (TD202 only)	Center

Azimuth Adjustment

When the maximum level point of R channel does not equal that L channel, connect the oscilloscope as shown in Fig. 3 and proceed with azimuth adjustment so that L and R channels are in phase.

- a) Connect L cnannel tape out to "X (or V)" and R channel to "Y (or H)". Observe the lissajouss waveform.
- b) Set L and R channels to monaural. Adjust vertical and horizontal gain so that the waveform becomes 45 degree.
- c) Adjust azimuth so that the measurement of "a" becomes maximum and the measurement of "b" becomes minimum against the 45 degree line.





ALIGNMENT PROCEDURES (REFER TO PAGES 24, 25, 27 AND 28 FOR TD102 OR 30, 31, 33 AND 34 FOR TD202)

■ CASSETTE MECHANISM CONFIRMATION

Make sure to confirm conditions of the cassette mechanism as follows before adjustment.

1. Confirmation of erroneous erase preventive function

- The switch should turn ON when a tape with erroneous erase preventive pawl is inserted. (Use a tape which is 0.2mm smaller than the minimum size of 62.9mm or a MAZ-0184-C gauge one.)
- When the switch arm is moved back gradually from the ON position, the switch should turn OFF.

2. Confirmation of cassette pack detection function

- The switch should turn ON when a tape is inserted. (Use a tape whose minimum size is 63.5mm or a MAZ-0184-C gauge one.)
- When the switch arm is moved back gradually from the ON position, the switch should turn OFF.

3. Confirmation of eject function

- The cassette compartment opens smoothly and no abnormal noise should be heard while opening and closing.
- The eject lock arm opens smoothly without contacting the chassis and damper.
- The eject button can not be pressed during playback.

4. Confirmation of playback, fast forward and rewind functions

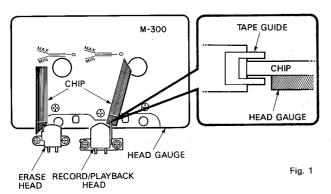
 The torque used in each of the playback, fast forward and rewind modes should be within specification.

 No abnormal noise should be heard during operation in any mode. The solenoid switching sound should not be considered as a noise.

Confirmation of positions of record/playback head and erase head

Head height

- a) Set the M-300 head gauge.
- b) Set the unit in the playback mode and place the adjustment chip on the head gauge as shown in the Fig. 1.
- The adjustment chip should not contact the tape guide of both record/playback head and erase head.



Head position

- a) Set the M-300 head gauge.
- b) Set the unit in the playback mode and place the adjustment chip on the head gauge as shown in the Fig. 2.
- With both record/playback head and erase head, the adjustment chip should be between MIN and MAX of the M-300 head gauge.

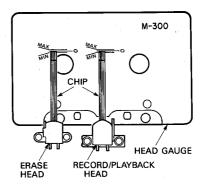


Fig. 2

■ ELECTRICAL ADJUSTMENT AND CONFIRMATION

1. Before adjustment

- Before electriacl adjustment, make sure that confirmations of the cassette mechanism are all completed.
- After the power switch is pushed on, wait for 10 minutes before measuring to be sure of the most stable operation.
- Since head magnetization, dust accumulations, etc. are likely to introduce errors in the various characteristics, it is very important that the heads are properly demagnetized and cleaned before commencing any adjustment, particularly frequency response and head azimuth adjusment.

2. Instruments required

- Low frequency oscillator
- AC VTVM or dual channel AC VTVM
- Oscilloscope
- Wow/flutter meter
- Frequency counter

3. Test tapes

• Azimuth adjustment	MTT-114
• Tape speed adjustment	
• Playback output level adjustmen	t
	MTT-150 or TCC-130
• Playback frequency characteristi	c confirmation

............ TCC-162C, TCC-262C

•	Reference tapes	
	LN	SCC-502
	CrO ₂	SCC-504
	METAL	SCC-565

Note:

C-90 differes with C-60 in the thickness and bias is of unequal, so adjust with the tape whose bias in of specified value.

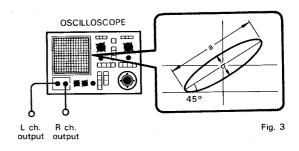
4. General conditions (unless otherwise noted)

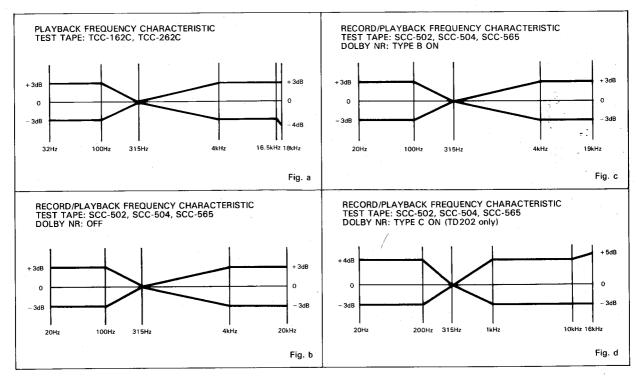
Controls and Switches	Settings
Dolby NR	Off
Input Level	Maximum
MPX Filter	Off
Bias Fine Trim (TD202 only)	Center

Azimuth Adjustment

When the maximum level point of R channel does not equal that L channel, connect the oscilloscope as shown in Fig. 3 and proceed with azimuth adjustment so that L and R channels are in phase.

- a) Connect L channel tape out to "X (or V)" and R channel to "Y (or H)". Observe the lissajouss waveform.
- b) Set L and R channels to monaural. Adjust vertical and horizontal gain so that the waveform becomes 45 degree.
- c) Adjust azimuth so that the measurement of "a" becomes maximum and the measurement of "b" becomes minimum against the 45 degree line.





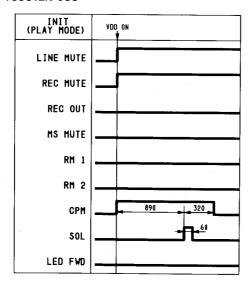
Step _.	Alignment	Instrument Require	Input Signal	Mode	Test Point	Adjustment	For
1	Azimuth	VTVM Oscilloscope Test tape (MTT-114)		РВ	TP1 TP2	Azimuth screw	Maximum output Refer to "Azimuth Adjustment" on page 9.
2	Tape speed	Frequency counter Test tape (MTT-111 or MTT-111D)		РВ	TP1 TP2	VR (built-in motor)	3000Hz ±10Hz Adjust at the center of test tape
3	Playback output level	VTVM Test tape (MTT-150 or TCC-130)		РВ	TP1 TP2	VR101 VR102	580mV (TD102)/300mV (TD202)
4	Playback frequency characteristic	VTVM Test tape (TCC-162 and TCC-262C)		РВ	OUTPUT jack	VR103 VR104	So that the frequency response is $-1dB \sim -2dB$ (TD102)/ $0.5dB \sim -1.5dB$ (TD202).
5	Bias trap	VTVM		REC/PB	TP3 TP4	L203 L204	Minimum output
1					TP5 TP6	VR253 VR254	75mV (TD102)/65mV (TD202) Tape selector is METAL position.
6 2	Bias level (pre-adjustment)	VTVM		REC/PB	TP5 TP6	VR251	45mV Tape selector is CrO ₂ position.
, 3					TP5 TP6	VR252	25mV (TD102)/27mV (TD202) Tape selector is LN position.
7	Bias frequency confirmation	Frequency counter		REC/PB	TP5		105kHz ± 5kHz Tape selector is METAL position.
8	Record level (pre-adjustment)	VTVM Blank tape (SCC-504)	Apply 400Hz signal to INPUT jack. Set INPUT LEVEL knob so that TP1 (TD102)/TP7 (TD202) and TP2 (TD102)/TP8 (TD202) voltage is 580mV (TD102)/300mV (TD202) in REC-PAUSE mode.	REC/PB	TP1 TP2	VR201 VR202	580mV (TD102)/300mV (TD202) Tape selector is CrO2 position.
9	Peaking coil (pre-adjustment)	VTVM Blank tape (SCC-565)	Apply 400Hz signal to INPUT jack. Set INPUT LEVEL knob so that TP1 (TD102)/TP7 (TD202) and TP2 (TD102)/TP8 (TD202) voltage is 580mV (TD102)/300mV (TD202) – 25dB in REC mode.	REC/PB	TP5 TP6	L201 L202	So that output becomes maximum at 20kHz input. Remove the R252 (TD102 only). Open the lid of connector (CN110) and then disconnect the lead wires (TD202 only). Tape selector is METAL position.
10	Record/playback equalizer frequency characteristic (pre-adjustment)	VTVM Blank tape (SCC-565)	Apply 400Hz signal to INPUT jack. Set INPUT LEVEL knob so that TP1 (TD102)/TP7 (TD202) and TP2 (TD102)/TP8 (TD202) voltage is 580mV (TD102)/300mV (TD202) — 25dB in REC-PAUSE mode. Then adjust with a 20Hz to 30kHz sweep signal.	REC/PB	OUTPUT jack	VR253 VR254	So that the record/playback frequency response is flat (at least within the range in Fig. b). Tape selector is METAL position.
1						VR251,L202	So that the record/playback frequency response is flat (at least within the range in Fig. b).
		VTVM	Apply 400Hz signal to INPLIT iack. Set INPLIT LEVEL know so that			VR253,L201	Tape selector is CrO2 position.
11 2	Record/playback equalizer frequency characteristic	Blank tapes (Metal SCC-565) (CrO2 SCC-504)	Apply 400Hz signal to INPUT jack. Set INPUT LEVEL knob so that TP1 (TD102)/TP7 (TD202) and TP2 (TD102)/TP8 (TD202) voltage is 580mV (TD102)/300mV (TD202) – 25dB in REC-PAUSE mode. Then adjust with a 20Hz to 30kHz sweep signal.	REC/PB	OUTPUT jack	VR252	So that the record/playback frequency response is flat (at least within the range in Fig. b). Tape selector is LN position.
3	·	\LN SCC-502/	*.		·	check	Tape selector is METAL position.
4						VR253	Tape selector is LN position. So that the record/playback frequency response of L ch. at METAL position and that of L ch. at LN position are balanced.
12	Record level	VTVM Blank tape (SCC-504)	Apply 400Hz signal to INPUT jack. Set INPUT LEVEL knob so that TP1 (TD102)/TP7 (TD202) and TP2 (TD102)/TP8 (TD202) voltage is 580mV (TD102)/300mV (TD202) in REC-PAUSE mode.	REC/PB	TP1 TP2	VR201 VR202	580mV (TD102)/300mV (TD202) Tape selector is CrO2 position.
13	Record level	VTVM Blank tapes (Metal SCC-565) (LN SCC-502)	Apply 400Hz signal to INPUT jack. Set INPUT LEVEL knob so that TP1 (TD102)/TP7 (TD202) and TP2 (TD102)/TP8 (TD202) voltage is 580mV (TD102)/300mV (TD202) in REC-PAUSE mode.	REC/PB	TP1 TP2		$580\text{mV}\pm1\text{dB}$ (TD102)/300mV $\pm1\text{dB}$ (TD202) This confirmation should be done at each tape selector position.
14	Record/playback equalizer frequency characteristic confirmation	VTVM Blank tapes (Metal SCC-565) CrO2 SCC-504 LN SCC-502	Apply 400Hz signal to INPUT jack. Set INPUT LEVEL knob so that TP1 (TD102)/TP7 (TD202) and TP2 (TD102)/TP8 (TD202) voltage is 580mV (TD102)/300mV (TD202) — 25dB in REC-PAUSE mode. Then adjust with a 20Hz to 30kHz sweep signal.	REC/PB	OUTPUT jack		Confirm that the record/playback frequency response is within the range in Fig. c and Fig. d. If it is not within the specification, redo the adjustment and confirmation in steps 10, 11 and 12. This confirmation should be done at each tape selector position under each of the following conditions. Dolby B NR on and Dolby C NR on.
. 15	Meter level	VTVM	Apply 400Hz signal to INPUT jack. Set INPUT LEVEL knob so that TP1 (TD102)/TP7 (TD202) and TP2 (TD102)/TP8 (TD202) voltage is 580mV (TD102)/300mV (TD202) - 0.5dB in REC-PAUSE mode.	PAUSE	PEAK LEVEL METER	VR401 VR402	Adjust to the point where the OdB of the PEAK LEVEL METER lights.
16	MPX filter characteristic confirmation	VTVM	Apply 19kHz and 15kHz signal to INPUT jack. Set INPUT LEVEL knob so that TP1 (TD102)/TP7 (TD202) and TP2 (TD102)/TP8 (TD202) voltage is 580mV (TD102)/300mV (TD202) in REC-PAUSE mode.	REC- PAUSE	OUTPUT jack		Confirm that attenuation of 15kHz and 19kHz is within the specification when MPX FILTER on.
17	Anti-skewing level confirmation (TD202 only)	VTVM	Apply 400Hz signal to INPUT jack. Set INPUT LEVEL knob so that TP7 and TP8 voltage is 300mV - 25dB in REC-PAUSE mode.	REC- PAUSE	TP201 TP202		Confirm that attenuation of 20kHz is maximum. Dolby C NR is on.

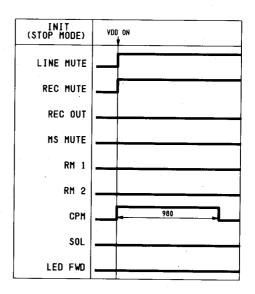
Step	Alignment	Instrument Require	Input Signal
1	Azimuth	VTVM Oscilloscope Test tape (MTT-114)	Augusta All Remark on Propogram (europe and a la fine de proposition de la fine de la manuel de montre de Môn e
2	Tape speed	Frequency counter Test tape (MTT-111 or MTT-111D)	
/3	Playback output level	VTVM Test tape (MTT-150 or TCC-130)	
4	Playback frequency characteristic	VTVM Test tape (TCC-162 and TCC-262C)	
5	Bias trap	VTVM	·
6 2	Bias level (pre-adjustment)	VTVM	
7	Bias frequency confirmation	Frequency counter	
8	Record level (pre-adjustment)	VTVM Blank tape (SCC-504)	Apply 400Hz signal to INPUT jack. Set INPUT LEVEL knob so TP1 (TD102)/TP7 (TD202) and TP2 (TD102)/TP8 (TD202) volta 580mV (TD102)/300mV (TD202) in REC-PAUSE mode.
.9	Peaking coil (pre-adjustment)	VTVM Blank tape (SCC-565)	Apply 400Hz signal to INPUT jack. Set INPUT LEVEL knob so TP1 (TD102)/TP7 (TD202) and TP2 (TD102)/TP8 (TD202) volta 580mV (TD102)/300mV (TD202) – 25dB in REC mode.
10	Record/playback equalizer frequency characteristic (pre-adjustment)	VTVM Blank tape (SCC-565)	Apply 400Hz signal to INPUT jack. Set INPUT LEVEL knob so TP1 (TD102)/TP7 (TD202) and TP2 (TD102)/TP8 (TD202) volta 580mV (TD102)/300mV (TD202) – 25dB in REC-PAUSE mode. Then adjust with a 20Hz to 30kHz sweep signal.
11 2 3 3	Record/playback equalizer frequency characteristic	VTVM Blank tapes (Metal SCC-565 (Cr02 SCC-504 LN SCC-502)	Apply 400Hz signal to INPUT jack. Set INPUT LEVEL knob so TP1 (TD102)/TP7 (TD202) and TP2 (TD102)/TP8 (TD202) volta 580mV (TD102)/300mV (TD202) – 25dB in REC-PAUSE mode. Then adjust with a 20Hz to 30kHz sweep signal.
12	Record level	VTVM Blank tape (SCC-504)	Apply 400Hz signal to INPUT jack. Set INPUT LEVEL knob so TP1 (TD102)/TP7 (TD202) and TP2 (TD102)/TP8 (TD202) volta 580mV (TD102)/300mV (TD202) in REG-PAUSE mode.
13	Record level	VTVM Blank tapes (Metal SCC-565) (LN SCC-502)	Apply 400Hz signal to INPUT jack. Set INPUT LEVEL knob so TP1 (TD102)/TP7 (TD202) and TP2 (TD102)/TP8 (TD202) volta 580mV (TD102)/300mV (TD202) in REC-PAUSE mode.
14	Record/playback equalizer frequency characteristic confirmation	VTVM Blank tapes (Metal SCC-565 CrO2 SCC-504 LN SCC-502	Apply 400Hz signal to INPUT jack. Set INPUT LEVEL knob so TP1 (TD102)/TP7 (TD202) and TP2 (TD102)/TP8 (TD202) volta 580mV (TD102)/300mV (TD202) – 25dB in REC-PAUSE mode. Then adjust with a 20Hz to 30kHz sweep signal.
15	Meter level	VTVM	Apply 400Hz signal to INPUT jack. Set INPUT LEVEL knob so TP1 (TD102)/TP7 (TD202) and TP2 (TD102)/TP8 (TD202) volta 580mV (TD102)/300mV (TD202) - 0.5dB in REC-PAUSE mode
16	MPX filter characteristic confirmation	VTVM	Apply 19kHz and 15kHz signal to INPUT jack. Set INPUT LEVEL so that TP1 (TD102)/TP7 (TD202) and TP2 (TD102)/TP8 (TD voltage is 580mV (TD102)/300mV (TD202) in REC-PAUSE mod
17	Anti-skewing level confirmation (TD202 only)	VTVM	Apply 400Hz signal to INPUT jack. Set INPUT LEVEL knob so TP7 and TP8 voltage is 300mV – 25dB in REC-PAUSE mode.

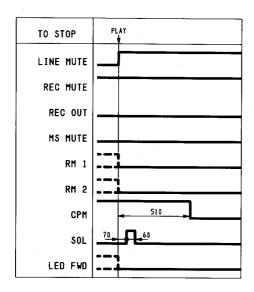
	Mode	Test Point	Adjustment	For
	РВ	TP1 TP2	Azimuth screw	Maximum output Refer to "Azimuth Adjustment" on page 9.
	РВ	TP1 TP2	VR (built-in motor)	3000Hz ±10Hz Adjust at the center of test tape
	РВ	TP1 TP2	VR101 VR102	580mV (TD102)/300mV (TD202)
	РВ	OUTPUT jack	VR103 VR104	So that the frequency response is $-1dB \sim -2dB$ (TD102)/ $0.5dB \sim -1.5dB$ (TD202).
	REC/PB	TP3 TP4	L203 L204	Minimum output
		TP5 TP6	VR253 VR254	75mV (TD102)/65mV (TD202) Tape selector is METAL position.
	REC/PB	TP5 TP6	VR251	45mV Tape selector is CrO2 position.
:		TP5 TP6	VR252	25mV (TD102)/27mV (TD202) Tape selector is LN position.
	REC/PB	TP5		105kHz ±5kHz Tape selector is METAL position.
(nob so that 2) voltage is	REC/PB	TP1 TP2	VR201 VR202	580mV (TD102)/300mV (TD202) Tape selector is CrO2 position.
knob so that (2) voltage is	REC/PB	TP5 TP6	L201 L202	So that output becomes maximum at 20kHz input. Remove the R252 (TD102 only). Open the lid of connector (CN110) and then disconnect the lead wires (TD202 only). Tape selector is METAL position.
knob so that 2) voltage is mode.	REC/PB	OUTPUT jack	VR253 VR254	So that the record/playback frequency response is flat (at least within the range in Fig. b). Tape selector is METAL position.
			VR251,L202	So that the record/playback frequency response is flat (at
	REC/PB		VR253,L201	least within the range in Fig. b). Tape selector is CrO2 position.
knob so that (2) voltage is E mode.		EC/PB OUTPUT jack	VR252	So that the record/playback frequency response is flat (at least within the range in Fig. b). Tape selector is LN position.
,			check	Tape selector is METAL position.
		·	VR253	Tape selector is LN position. So that the record/playback frequency response of L ch. at METAL position and that of L ch. at LN position are balanced.
knob so that 2) voltage is	REC/PB	TP1 TP2	VR201 VR202	580mV (TD102)/300mV (TD202) Tape selector is CrO2 position.
knob so that (2) voltage is	REC/PB	TP1 TP2		$580 \text{mV} \pm 1 \text{dB} \text{ (TD102)/300 mV} \pm 1 \text{dB} \text{ (TD202)}$ This confirmation should be done at each tape selector position.
knob so that 12) voltage is E mode.	REC/PB	OUTPUT jack		Confirm that the record/playback frequency response is within the range in Fig. c and Fig. d. If it is not within the specification, redo the adjustment and confirmation in steps 10, 11 and 12. This confirmation should be done at each tape selector position under each of the following conditions. Dolby B NR on and Dolby C NR on.
knob so that (2) voltage is E mode.	REC- PAUSE	PEAK LEVEL METER	VR401 VR402	Adjust to the point where the OdB of the PEAK LEVEL METER lights.
LEVEL knob TP8 (TD202) ISE mode.	REC- PAUSE	OUTPUT jack		Confirm that attenuation of 15kHz and 19kHz is within the specification when MPX FILTER on.
knob so that mode.	REC- PAUSE	TP201 TP202		Confirm that attenuation of 20kHz is maximum. Dolby C NR is on.

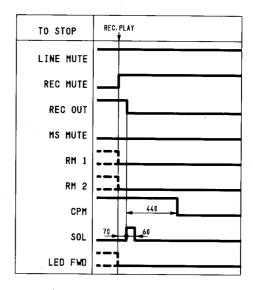
TIMING CHART

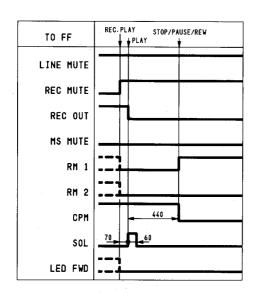
IC801: TC9312N-038

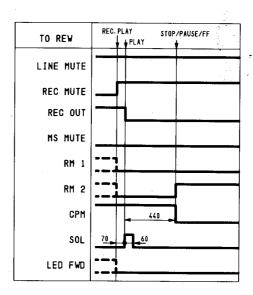


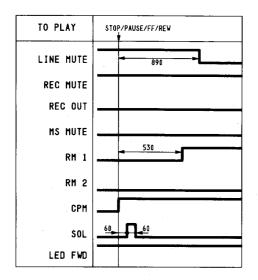


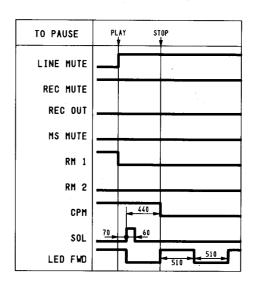


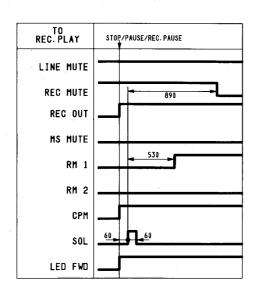


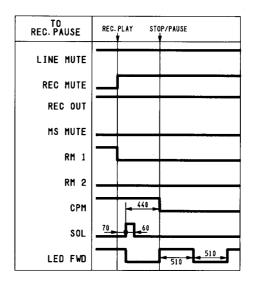








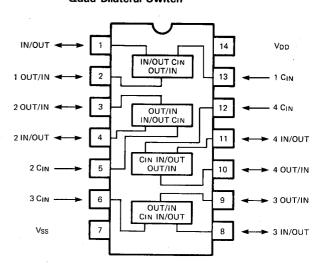




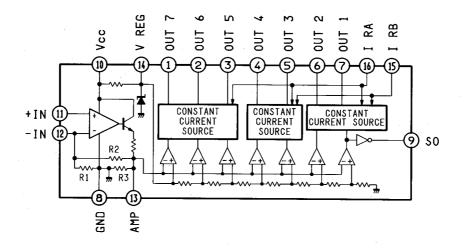
IC BLOCK DIAGRAM

IC201 : M5219P
Dual Operational Amplifier

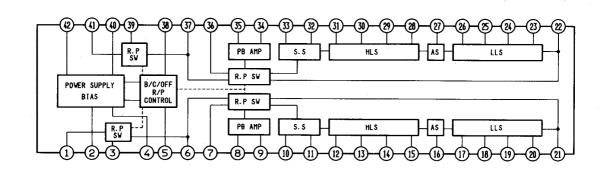
IC301 : M4066BP (TD102 only) Quad Bilateral Switch



IC401, 402 : IR2E19 7-Dot LED Driver

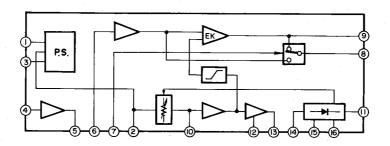


IC501 : HA12088NT (for TD202) Dolby B-C NR

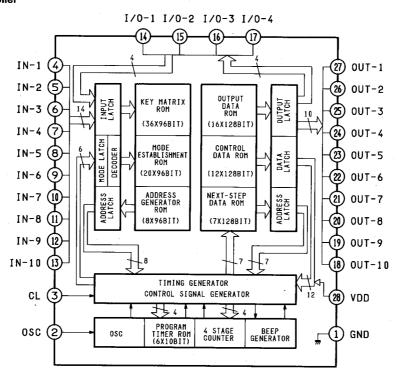


IC501, 502 : TA7629P (for TD102)

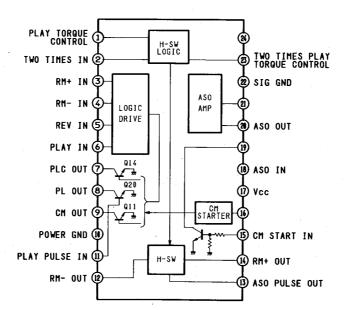
Dolby NR

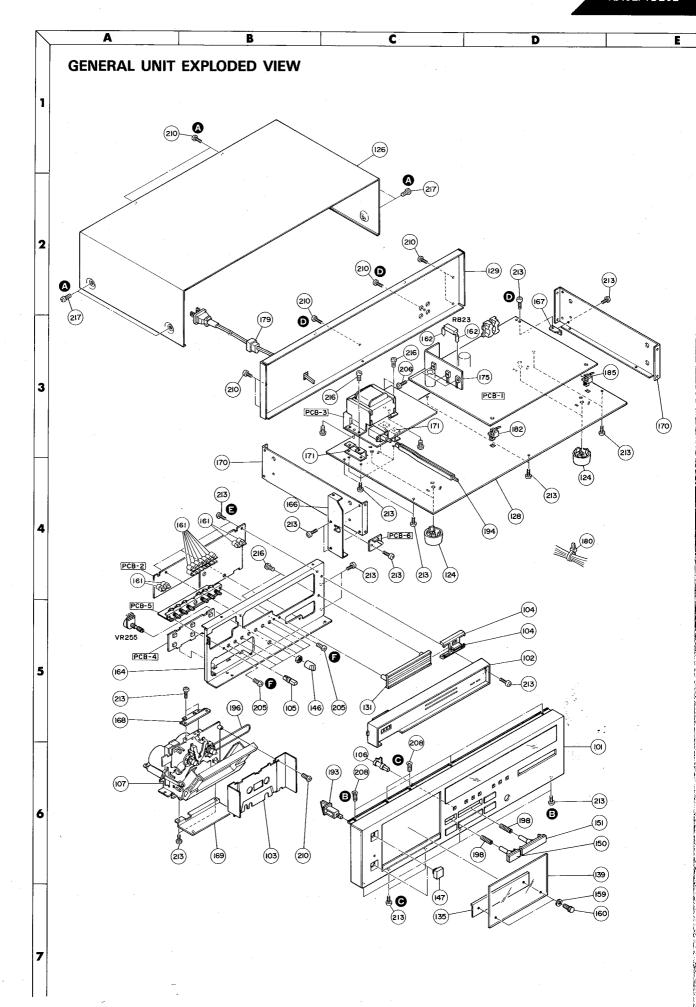


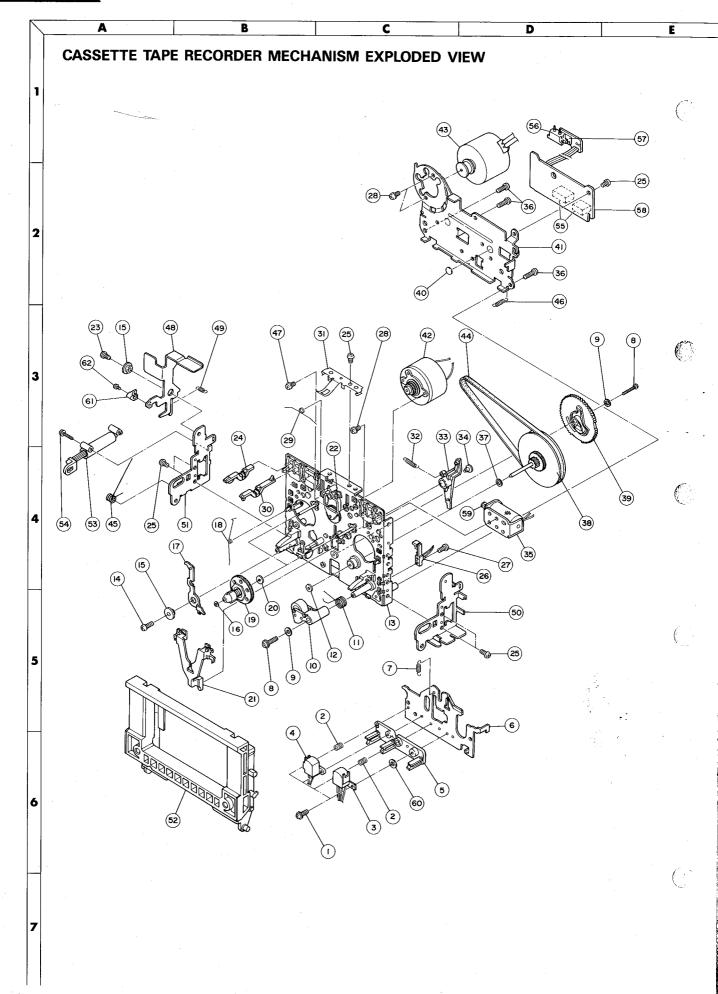
IC801 : TC9312N-038 Logic Controller



IC802 : TA7780BN Motor Driver







GENERAL UNIT PARTS LIST

Ref. No.	Part No.	Description	Ref. No.	Part No.	Description
101	A443-TD202A	Front Panel Ass'y (for TD202)	147	1662-25401VN	Push Button, Power, Eject
101	A443-TD102A	Front Panel Ass'y (for TD102)	147	1662-25402	Push Button, Power, Eject
101	A443-TD202B	Front Panel Ass'y (for TD202) ■BD ■GB	150	1660-00801	Push Button, REW, F. FWD, Rec./Pause, Rec. Mute
101	A443-TD102B	Front Panel Ass'y (for TD102) GB	150	1660-00802	Push Button, REW, F. FWD,
102	A513-TD202A	Plate Ass'y			Rec./Pause, Rec. Mute
103	A514-TD202A	Dressing Plate Ass'y			
104	A642-TD202A	Knob Ass'y, Input Level	151	1660-00901	Push Button, Play, Stop
104	A642-TD202B	Knob Ass'y, Input Level ◆BI〉 ◆GB〉	151	1660-00902	Push Button, Play, Stop
105	A662-TD202A	Push Button Ass'y, Bias/	159	2114-01224	Bushing
		Equalization, Dolby NR,	. 160	2310-7025	Special Screw 🕕 🜀
		NR Type (TD202 only), MPX	160	2310-7026	Special Screw (BK) (GB)
		Filter G	161	2132-01405	Spacer
105	A662-TD202C	Push Button Ass'y, Bias/	162	2132-7049	Spacer (TD202 only)
		Equalization, Dolby NR,	164	2211-7280	Chassis
		NR Type (TD202 only), MPX	166	2219-8095	Brecket
		Filter (BK) (GB)	167	2219-8096	Brecket
106	A662-TD202B	Push Button Ass'y, Counter	168	2219-8097	Brecket
		Reset U G	169	2219-8098	Brecket
106	A662-TD202D	Push Button Ass'y, Counter	170	2219-8099	Brecket
		Reset (BK) (GB)	171	2219-8100	Brecket
107	3112-13701	Cassette Tape Recorder	175	2222-7186	Heat Sink
		Mechanism Ass'y	179	2240-364	Holder
124	1319-0139	Foot	180	2240-7120	Holder
126	1414-06701	Cabinet Top	182	2240-7264	Holder
128	1423-02601	Cabinet Bottom	185	2240-7110	Holder
129	1424-18401	Cabinet Back (for TD202)	193	2601-7160	Shaft
			194	2601-7161	Shaft
129	1424-18403	Cabinet Back (for TD102)	196	2642-01440	Belt
			198	2651-2101721	Spring
129	1424-18402	Cabinet Back (for TD202)	205	2327-300529	Screw (3×5 mm)
		G GB	206	2327-300829	Screw (3×8 mm)
129	1424-18404	Cabinet Back (for TD102)	208	2343-300627	Screw (3×6 mm)
		G GB	210	2347-300846	Screw (3×8mm)
131	1442-12101	Panel, Input Level 🕕 🜀	213	2347-300626	Screw (3×6mm)
131	1442-12102	Panel, Input Level 🕬 📵	216	2347-300826	Screw (3×8mm)
135	1514-17002	Dressing Plate	217	2347-400646	Screw (4×6mm)
135	1514-17004	Dressing Plate (B)		1111-J30245	Owner Guide
139	1531-08501	Door Cover		1111-J30246	Owner Guide G GB
146	1632-13902VN	Rotary Knob, Bias Fine Trim (TD202 only)		1221-717184 1222-7289	Packing Box Packing Cushion
146	1632-13901	Rotary Knob, Bias Fine Trim (TD202 only)			- -

NOTES

Parts with the following marks are used only $\hat{\mathbf{n}}$ the models intended for particular markets:

U : U.S.A. model :

BK : U.S.A. model Black Version

G : General model

GB: General model Black Version

CASSETTE TAPE RECORDER MECHANISM PARTS LIST

Ref. No.	Part No.	Description	Ref. No.	Part No.	Description
1	FG137-18	Screw	33	F148S025	Play Arm Ass'y
2	FK21U-11	Spring	34	FC728-12	Сар
3	FUI6B-II	REC/PB Head	35	F265-240	Solenoid
4	FU192-11	Erase Head	36	UG12H-14	Screw (2.6×8mm)
5	FD33C-11	Head Spacer	37	FJ111-30	Non-metal Washer
6 .	FC38N-46	Head Base	38	FR18M-15	Flywheel Ass'y
7	FK22L-11	Head Base Spring	38	FR19S-12	Flywheel Ass'y
8	UC12H-20	Screw (2×13mm)	39	FD34K-16	Cum Gear
9	MJ112-22	Washer	40	UJIIS-II	Spacer
10	F014-095	Pinch Roller	41	FC38P-35	Flywheel Bracket
11	FK22B-1-1	Pinch Roller Spring	42	F064S235	Reel Motor
12	FJ141-11	Oil Seal	43	F064-157	Capstan Motor
13	F112-083	Chassis	44	FF15R-11	Belt
14	UG12H-24	Screw (3×10mm)	45	FK21G-11	Door Spring
15	FM296-11	Spacer	46	FK22K-12	Ground Spring
16	FJ111-17	Non-metal Washer	47	FG114-14	Screw
17	FC39S-14	Eject Prevention Arm	48	FC45V-11	Eject Arm
18	FK22P-15	Eject Prevention Spring	49	FK23R-11	Eject Arm Spring
19	F105-027	Reel Ass'y	50	FC33K-14	Dumper Bracket (R)
20	UJ12V-11	Non-metal Washer	51	FC33L-14	Dumper Bracket (L)
21	FD35T-11	Hold Lever	52	FD35C-12	Door Frame Ass'y
22	F017-044	Idler Ass'y	53	FP472-11	Dumper Ass'y
23	KG194-12	Screw	54	UG12R-11	Screw
24	FD32C-32	Switch Arm, Pack	55	F067-073	Connector
25	KG194-46	Screw (3×4mm)	56	UE14U-16	Push Switch
26	UE14T-II	Leaf Switch	57	FD37E-12	Switch Spacer
27	UG12H-23	Screw (2×7mm)	58	FP15W-24	Relay P. C. Board
28	UGIIS-14	Screw	59	PL358-12	Plunger
29	FK22E-11	Hold Spring	60	FJ111-19	Non-metal Washer
30	FD32B-32	Switch Arm, Rec.	61	FD37N-11	Fook
31	FC40N-32	Spring, Cassette	62	KG194-34	Screw
32	FK22G-13	Play Arm Spring			

ELECTRICAL PARTS LIST

	Ref. No.	Part No.	Description	Ref. No	o. Part No.	Description
		CHASSIS I	WISCELLANEOUS	C214	5345-226C0952	22μF/16V, EC
				C219	5345-225F0952	2.2μF/50V, EC
Δ	PΙ	4161-71151	Power Cord	C220	5345-225F0952	2.2μF/50V, EC
Δ	PΙ	4161-7256	Power Cord G GB	C223	5345-227C041	220µF/16V, EC
	FUI	5732-102052	Fuse, IA, 250V G GB	C227	5359-3315851	330pF/100V, PC
	VR255	5113-2028221	Control, 2kΩB, Bias	C228	5359-3315851	330pF/100V, PC
		4161.71104	Fine Trim (TD202 only)	C251	5345-107C041	100μF/16V, EC
		4161-71184	Connection Cord (Accessory)	C253	5353-101534	100pF/500V, MC
	iio servae aberre			C254 C301	5353-101534	100pF/500V, MC
		PCB-I MAIN P	C BOARD	C302	5345-105F0951 5345-105F0951	$I\mu F/50V$, EC (TD102 only) $I\mu F/50V$, EC (TD102 only)
				C305	5345-476C041	$47\mu F/16V$, EC (TD102 only)
		RESISTORS		C501	5345-105F0951	1μ F/50V, EC (for TD102)
Δ	R51	5102-1004715	10Ω, 1/4W, FR	C502	5345-105F0951	IμF/50V, EC (for TD102)
Φ	R52	5102-1004715	10Ω, 1/4W, FR	C503	5345-106C0952	$10\mu F/16V$, EC (for TD102)
	R105	5174-223381	22kΩ, I/4W, MR	C504	5345-106C0952	10μF/16V, EC (for TD102)
	R106	5174-223381	22kΩ, I/4W, MR	C505	5345-106C0952	10μF/16V, EC (for TD102)
	R107	5174-331381	330Ω, I/4W, MR	C506	5345-106C0952	10μF/16V, EC (for TD102)
•	R108	5174-331381	330Ω, I/4W, MR	C507	5345-106C0952	10μF/16V, EC (for TD202)
Δ	R137 R252	5102-4R74715 5102-4R74715	4.7Ω, I/4W, FR 4.7Ω, I/4W, FR (for TD202)	C508	5345-106C0952	10μF/16V, EC (for TD202)
<u> </u>	R252	5102-1004715	4.74, 1/4W, FR (for TD102)	C509	5345-477C041	470µF/16V, EC (for TD202)
<u> </u>	R513	5174-332381	$3.3k\Omega$, $1/4W$, MR (for TD102)	C511	5345-106C0952	10μF/16V, EC (for TD202)
	R514	5174-332381	$3.3k\Omega$, $1/4W$, MR (for TD102)	C511 C512	5359-3325851 5345-106C0952	3300pF/100V, PC (for TD102)
Δ	R521	5102-4704715	47Ω, I/4W, FR (for TD102)	C512	5359-3325851	10μF/16V, EC (for TD202) 3300pF/100V, PC (for TD102)
Δ	R530	5102-1004715	10Ω, I/4W, FR (TD202 only)	C513	5345-106C0951	10μF/16V, EC (for TD202)
$\Delta\!$	R821	5102-4704715	47Ω, I/4W, FR	C514	5345-106C0951	10μF/16V, EC (for TD202)
Δ	R823	5102-2205711	22Ω, IW, FR	C515	5345-477C041	470μF/16V, EC (for TD202)
				C516	5345-226C041	22μF/16V, EC (for TD202)
		CONTROLS		C517	5345-106C041	10μF/16V, EC (for TD202)
	VRIOI	5101-20301927	20k ΩB	C518	5345-106C041	10μ F/16V, EC (for TD202)
	VR102	5101-20301927	20k ΩB	C519	5345-226C0951	22μF/16V, EC (for TD202)
	VR103	5101-20201927	2kΩ	C520	5345-226C0951	22μF/16V, EC (for TD202)
	VR104 VR201	5101-20201927	2kΩ	C521	5359-103741	0.01pF/100V, PC (for TD202)
	VR201 VR202	5101-20271920 5101-20271920	2kΩB 2kΩB	C521	5345-106C0952	10μF/16V, EC (for TD102)
	VR251	5101-10371920	ΙοκΩΒ	C522 C522	5359-103741 5345-106C0952	0.01pF/100V, PC (for TD202)
	VR252	5101-50271920	5kΩB	C523	5345-105F0952	$10\mu\text{F}/16\text{V}$, EC (for TD102) $1\mu\text{F}/50\text{V}$, EC (for TD202)
	VR253	5101-10401927	100kΩB	C523	5345-104F0952	$0.1\mu F/50V$, EC (for TD102)
	VR254	5101-10401927	100kΩB	C524	5345-105F0952	IμF/50V, EC (for TD202)
				C524	5345-104F0952	0.1μF/50V, EC (for TD102)
		CAPACITORS		C525	5345-334F0952	$0.33\mu F/50V$, EC (for TD102)
	C3	5345-228D041	2200μF/25V, EC	C526	5345-334F0952	$0.33\mu F/50V$, EC (for TD102)
	C4	5345-108C041	1000μF/16V, EC	C527	5345-684F0952	$0.68 \mu F/50 V$, EC (for TD202)
	C5	5345-477C041	470μF/16V, EC	C527	5345-106C0951	10μF/16V, EC (for TD102)
	C6 C8	5345-107C041 5345-478C041	100μF/16V, EC 4700μF/16V, EC	C528	5345-684F0952	0.68μF/50V, EC (for TD202)
	C51	5345-226D041	22μF/25V, EC	C528 C529	5345-106C0951	10μF/16V, EC (for TD102)
	C52	5345-226D041	22μF/25V, EC	C529	5345-225F0952 5345-106C0951	2·2μF/50V, EC (for TD202) 10μF/16V, EC (for TD102)
	CIOI	5353-101534	100pF/500V, MC	C530	5345-225F0952	2.2μF/50V, EC (for TD102)
	C102	5353-101534	100pF/500V, MC	C530	5345-106C0951	10μF/16V, EC (for TD102)
	C103	5345-226C0951	22μF/16V, EC	C531	5345-477C041	470µF/16V, EC (for ID102)
	C104	5345-226C0951	22μF/16V, EC	C532	5345-477C041	470µF/16V, EC (for TD102)
	C105	5345-107B041	100μF/10V, EC	C533	5359-103741	0.01pF/100V, PC (for TD202)
	C106	5345-107B041	100μF/10V, EC	C533	5345-227C041	220µF/16V, EC (for TD102)
	C107	5359-6825851	6800pF/100V, PC	C534	5359-103741	0.01pF/100V, PC (for TD202)
	C108	5359-6825851	6800pF/100V, PC 6800pF/100V, PC	C534	5345-227C041	220μF/16V, EC (for TD102)
	CIIO	5359-6825851 5359-6825851	6800pF/100V, PC	C535	5345-226C041	22μF/16V, EC (for TD102)
	CIII	5345-106C0951	10μF/16V, EC	C537 C537	5345-684F0952	0.68μF/50V, EC (for TD202)
	CIIZ	5345-106C0951	10μF/16V, EC	C537	5345-335F041 5345-684F0952	$3.3\mu F/50V$, EC (for TD102)
	C113	5359-1225851	1200pF/100V, PC	C539	5345-884F0952 5345-225F0952	0.68μF/50V, EC (TD202 only) 2.2μF/50V, EC (TD202 only)
	C114	5359-1225851	1200pF/100V, PC	C540	5345-225F0952	2.2μF/50V, EC (TD202 only) 2.2μF/50V, EC (TD202 only)
	C115	5345-477C041	470µF/16V, EC	C541	5345-106C0952	10μF/16V, EC (TD202 only)
	C116	5345-336C041	33μF/16V, EC	C542	5345-106C0952	10μF/16V, EC (TD202 only)
	C201	5359-6815851	680pF/100V, PC	C543	5345-106C0952	10μF/16V, EC (TD202 only)
	C202	5359-6815851	680pF/100V, PC	C544	5345-106C0952	10µF/16V, EC (TD202 only)
	C209	5345-105F0952	IμF/50V, EC	C569	5345-335F041	3.3μF/50V, EC (TD202 only)
	C210	5345-105F0952	IμF/50V, EC	C701	5345-475D041	4.7μF/25V, EC
	C211 C212	5345-226C041	22μF/16V, EC	C803	5345-106C041	10μF/16V, EC
	C212	5345-226C041 5345-226C0952	22μF/16V, EC 22μF/16V, EC	C804	5345-475D041	4.7μF/25V, EC
	0213	3343-77600337	22μ1 / 10V, EU	C805	5345-107B041	100μF/10V, EC

Ref. No.	Part No.	Description	Ref. No.	Part No.	Description
C806	5345-476B041	47μF/10V, EC	Q711	5613-3327(B)	2SC3327(B)
C807	5345-475D041	4.7μF/25V, EC	Q712	5613-3327(B)	2SC3327(B)
C808	5345-107B041	100μF/10V, EC	Q713	5613-RTIN241S	RTIN24IS
C809	5345-106C041	10μF/16V, EC	Q713 Q714		
C810	5345-106C041	10μF/16V, EC		5611-RT1P241S	RTIP24IS
C811	5345-107B041	100µF/10V, EC	Q801	5613-RT1N241S	RTIN24IS
			Q803	5613-RT1N241S	RTIN24IS
C812	5345-477B041	470μF/IOV, EC	Q804	5613-RT1N241S	RTIN241S
C813	5345-335F041	3.3μ F/50V, EC	Q805	5611-RT1P241S	RTIP24IS
			Q806	5611-RT1P241S	RTIP24IS
	INTEGRATED CIR		Q807	5611-1115(F)	2SA1115(F) or (E)
IC201	5652-M5219P	M5219P	Q808	5611-1335(BL)	2SA1335(BL) or (GR)
IC301	5654-M4066BP	M4066BP (TD102 only)	Q809	5613-2603(F)	2SC2603(F) or (E)
IC501	5652-HA12088	HA12088NT (for TD202)	Q810	5611-1286(H)	2SA1286(H) or (G)
IC501	5652-TA7629P	TA7629P (for TD102)	Q811	5613-3422(Y)	2SC3422(Y) or (O)
IC502	5652-TA7629P	TA7629P (TD102 only)	Q812	5613-3246(H)	2SC3246(H) or (G)
IC801	5654-9312-038	TC9312N-038	Q813	5613-RT1N241S	RTIN24IS
IC802	5654-TA7780BN	TA7780BN	Q814	5613-RT1N241S	RTIN241S
	TRANSISTORS			DIODES	
Q١	5611-1359(Y)	2SA1359(Y) or (O)	D.I		OFFCOR
Q2	5611-1359(Y)	2SA1359(Y) or (O)	DI	5632-S5566B	S5566B ·
Q2 Q3	5613-2603(F)	2SC2603(F) or (E)	D2	5632-S5566B	S5566B
Q3 Q4			D3	5632-S5566B	S5566B
	5613-2603(F)	2SC2603(F) or (E)	D4	5632-S5566B	S5566B
Q5	5613-2603(F)	2SC2603(F) or (E)	D12	5635-HZ12C2L	ZD, HZ12C2L
Q6	5613-2603(F)	2SC2603(F) or (E)	D13	5635-HZ12C3L	ZD, HZ12C3L
Q51	5611-1115(F)	2SA1115(F)	D14	5632-S5566B	S5566B
Q101	5613-1775(F)	2SC1775(F)	D51	5636-MC921	MC921
Q102	5613-1775(F)	2SC1775(F)	D52	5635-RD12EB2	ZD, RD12EB2
Q103	5613-1775(F)	2SC1775(F)	D53	5631-182473	IS2473
Q104	5613-1775(F)	2SC1775(F)	D101	5635-HZ11B2L	ZD, HZ11B2L
Q105	5613-2320L(F)	2SC2320L(F)	D251	5631-1S2473	IS2473
Q106	5613-2320L(F)	2SC2320L(F)	D706	5631-1S2473	152473
Q107	5613-RT1N241S	RTIN24IS	D707	5631-182473	IS2473
Q108	5613-RT1N241S	RTIN24IS	D710	5631-182473	IS2473
Q109	5613-2603(F)	2SC2603(F) or (E)	D807	5631-182473	IS2473
Q201	5613-RTIN241S	RTIN24IS	D808	5631-1S2473	
Q202	5613-RTIN241S	RTIN24IS	D809		IS2473
Q203	5613-RT1N241S	RTIN241S		5631-182473	182473
Q203	5613-RTIN241S	RTIN2413	D810	5631-182473	IS2473
Q204 Q205			D811	5631-182473	IS2473
	5613-RTIN241S	RTIN241S	D812	5631-IS2473	IS2473
Q206	5613-RTIN241S	RTIN24IS	D813	5631-IS2473	IS2473
Q251	5613-3246(H)	2SC3246(H) or (G)	D815	5635-RD2R7EB1	ZD, RD2.7EB1
Q252	5613-RTIN241S	RTIN24IS	D816	5635-RD3R9EB2	ZD, RD3.9EB2
Q253	5613-RT1N241S	RTIN24IS	D817	5635-RD6R2EB2	ZD, RD6.2EB2
Q254	5613-RT1N241S	RTIN24IS	D818	5635-RD8R2EB1	ZD, RD8.2EB1
Q255	5613-3246(H)	2SC3246(H) or (G)	D819	5632-S5566B	S5566B
		(TD202 only)	D820	5631-182473	IS2473
Q325	5613-RT1N241S	RTIN241S (TD102 only)	D821	5635-RD3R9EB2	ZD, RD3.9EB2
Q326	5613-RTIN241S	RTIN241S (TD102 only)	D822	5631-1S2473	IS2473
Q327	5613-2878(B)	2SC2878(B) (TD102 only)	D823	5631-152471	IS2471
Q328	5613-2878(B)	2SC2878(B) (TD102 only)	5020	3031-102471	102471
Q329	5611-RT1P241S	RTIP24IS (TD102 only)		COILS	Ţ,
Q501	5613-RT1N241S	RTIN24IS	LIOI	5995-363261	36mH
Q502	5613-RT1N241S	RTIN24IS	L101		36mH
Q502 Q503	5613-RT1N241S	RTIN24IS		5995-363261	36mH
Q503 Q504	5613-RT1N241S	RTIN241S	L201	5932-70523	6.8mH
Q504 Q505			L202	5932-70523	6.8mH
_	5613-2603(F)	2SC2603(F) (for TD202)	L203	5932-00224	
Q505	5613-2603(F)	2SC2603(F) or (E)	L204	5932-00224	
		(for TD102)	L501	5995-102269	ImH (TD202 only)
Q506	5613-2603(F)	2SC2603(F) (TD202 only)	L502	5995-102269	ImH (TD202 only)
Q507	5613-RT1N241S	RTIN241S (TD202 only)			
Q508	5613-2603(F)	2SC2603(F) or (E)		MISCELLANEOUS	
		(TD202 only)	JI	4484-45	4 Pin Jack, Input,
Q701	5613-3378(BL)	2SC3378(BL) or (GL)			Output
Q702	5613-3378(BL)	2SC3378(BL) or (GL)	LC501	5214-84	LC Composite (for TD202)
Q703	5613-3378(BL)	2SC3378(BL) or (GL)	LC501	5214-74	LC Composite (for TD102)
Q704	5613-3378(BL)	2SC3378(BL) or (GL)	LC502	5214-84	LC Composite (for TD202)
Q705	5613-2603(F)	2SC2603(F) or (E)	LC502	5214-64 5214-74	The state of the s
Q706	5613-2603(F)	2SC2603(F) or (E)			LC Composite (for TD102)
Q707	5613-2603(F)	2SC2603(F) or (E)	LC503	5214-77	LC Composite, 36mH
Q707 Q708	5613-2603(F)	2SC2603(F) or (E)		5014.77	(TD202 only)
			LC504	5214-77	LC Composite, 36mH
Q709	5613-3327(B)	2SC3327(B)			(TD202 only)
Q710	5613-3327(B)	2SC3327(B)	081	6171-1509	Bias Osc

Ref. No.	Part No.	<u>Description</u>		Ref. No.	Part No.	<u>Description</u>
LCN201 LCN202	4163-0518032 4163-0415032	CLW, 5 Pos. CLW, 4 Pos.		D419	5637-GL9PR9	L.E.D., GL9PR9, Red, Rec.
CNI	4443-0201102	Connector, 2 Pos.		D420	5637-GL9EG9	L.E.D., GL9EG9, Green,
CN2	4443-0601102	Connector, 6 Pos.		D-120		Play
CNIOI	4443-040185	Connector, 4 Pos.				, idy
CN 102	4443-070185	Connector, 7 Pos.				
CN 103	4443-050185	Connector, 5 Pos.	Í			Months for an experience of the state of the
CN 104	4443-040185	Connector, 4 Pos.	and the second	PC	B-3 POWER SUPPLY	P. C. BOARD
CN 105	4443-030185	Connector, 3 Pos.	E C			
		(TD202 only)			RESISTORS	
CN 107	4443-080185	Connector, 8 Pos.	Δ	RI2	5135-335583	3.3M Ω, 1/4W, CAR ઉ
CN 108	4443-030185	Connector, 3 Pos.	_			0.01.1.23, 17 11.1, 0.1.1.
CNIIO	4443-030185	Connector, 3 Pos.			CAPACITORS	
			Δ	CI	5361-1030419	0.01µF /AC 125V, CC
			Δ	CI	5352-1030958	0.01µF /AC 250V, MPC
	PCB-2 DISPLAY I	P. C. BOARD				G GB
	CONTROLS				MISCELLANEOUS	
VR351	5223-5037116	50k ΩA, Input Level	Δ	TI	5584-701520	Power Transformer
VR352	5223-5037116	50k ΩA, Input Level	Δ	TI	5584-702520	Power Transformer G GB
VR401	5101-10301927	I0kΩ	Φ	SI	4431-A01056	Push Switch, Power
VR402	5101-10301927	l0kΩ	Δ	S2	4411-1047111	Rotary Switch, Voltage
						Selector G GB
	CAPACITORS			CN109	4443-030185	Connector, 3 Pos.
C401	5345-225F0961	2.2μF/50V, EC			4472-0131	Fuse Holder G GB
C402	5345-225F0961	2.2μF/50V, EC				
C403	5345-475D0961	4.7μF/25V, EC				
C404	5345-475D0961	4.7μF/25V, EC	130			
C405	5345-106C0961	10μF/16V, EC	100	PCB-	I CONTROL SWITCH	ES P. C. BOARD
C406	5345-106C0961	10μF/16V, EC		CALLAND TRANSPORTED AND LAZITATION OF STATE	Part 1 New York of State	AND A THE AND REPORTED A PERSONAL PARTICIPATION AND A STATE OF THE PROPERTY OF
C407 C408	5345-22600961	22μF/16V, EC 22μF/16V, EC		0.705	DIODES	70 000 7500
0408	5345-226C0961	22με/16ν, ΕΟ		D705	5635-RD2R7EB2	ZD, RD2.7EB2
	INTEGRATED CIRC	PUTS		D801 D802	5631-182473	IS2473
IC401	5652-IR2E19	IR2E19		D802	5631-1S2473 5631-1S2473	1S2473 1S2473
IC402	5652-IR2E19	IR2E19		D804	5631-1S2473	1S2473
				D805	5631-1S2473	1S2473
	DIODES			D806	5631-IS2473	IS2473
D403	5637-GL9EG9	L.E.D., GL9EG9, Green,		2000	0001 102170	102470
		Peak Level Meter			MISCELLANEOUS	
D404	5637-GL9EG9	L.E.D., GL9EG9, Green,		S801	4431-A010131	Push Switch, F. FWD
		Peak Level Meter		S802	4431-A010131	Push Switch, Stop
D405	5637-GL9EG9	L.E.D., GL9EG9, Green,		S803	4431-A010131	Push Switch, Rec./Pause
		Peak Level Meter		S804	4431-A010131	Push Switch, Play
D406	5637-GL9EG9	L.E.D., GL9EG9, Green,		S805	4431-A010131	Push Switch, REW
		Peak Level Meter		S806	4431-A010131	Push Switch, Rec. Mute
D407	5637-GL9EG9	L.E.D., GL9EG9, Green,				
		Peak Level Meter				
D408	5637-GL9EG9	L.E.D., GL9EG9, Green,	12		FUNCTION CURTOR	LC B C BOARD
		Peak Level Meter		FV0.	FUNCTION SWITCH	ILS F. C. BOARD
D409	5637-GL9EG9	L.E.D., GL9EG9, Green,		S201	4431-06127159	Push Switch, LN (for TD202)
5416		Peak Level Meter		S201	4431-05107161	Push Switch, LN (for TD102)
D410	5637-GL9EG9	L.E.D., GL9EG9, Green,		S202	4431-06127159	Push Switch, CrO ₂
DALL	EC27 CL 0DD0	Peak Level Meter			,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	(for TD202)
D411	5637-GL9PR9	L.E.D., GL9PR9, Red,		S202	4431-05107161	Push Switch, CrO ₂
D412	5637-GL9PR9	Peak Level Meter				(for TD102)
D412	3037-GE3FR3	L.E.D., GL9PR9, Red, Peak Level Meter		S203	4431-06127159	Push Switch, Metal
D413	5637-GL9PR9	L.E.D., GL9PR9, Red,				(for TD202)
D413	3037-GESI 113	Peak Level Meter		S203	4431-05107161	Push Switch, Metal
D414	5637-GL9PR9	L.E.D., GL9PR9, Red,				(for TD102)
- 11-7	-55. WEUT 110	Peak Level Meter		S501	4431-06127159	Push Switch, MPX Filter
D415	5637-GL9PR9	L.E.D., GL9PR9, Red,				(for TD202)
		Peak Level Meter		S501	4431-05107161	Push Switch, MPX Filter
D416	5637-GL9PR9	L.E.D., GL9PR9, Red,				(for TD102)
		Peak Level Meter		S502	4431-06127159	Push Switch, Dolby NR Type
D417	5637-GL9EG9	L.E.D., GL9EG9, Green,				(TD202 only)
		Dolby NR B		S503	4431-06127159	Push Switch, Dolby NR
D418	5637-GL9HY9	L.E.D., GL9HY9, Amber,		CECC	4421 05107121	(for TD202)
		Dolby NR C (TD202 only)		S503	4431-05107161	Push Switch, Dolby NR
				ONLIGO	4442-020105	(for TD102)

CN106

4443-030185

Connector, 3 Pos.

Ref. No. Part No.

Description

PCB-6 POWER IND. P. C. BOARD

D421

5637-GL5HD10

L.E.D., GL5HD10, Red, Power

NOTES

Parts with the following marks are used only in the models intended for particular markets:

: U.S.A. model

B: U.S.A. model Black Version

G: General model

GB: General model Black Version

KEY TO ABBREVIATIONS

FR: Fuse Resistor MR: Metal Resistor CR : Cement Resistor CAR: Carbon Resistor

EC : Electrolytic Capacitor PC: Polypropylene Capacitor

MC: Mica Capacitor CC : Ceramic Capacitor

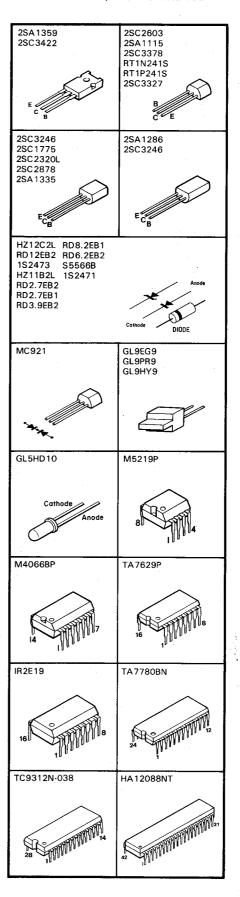
MPC: Metalized Polyester Capacitor

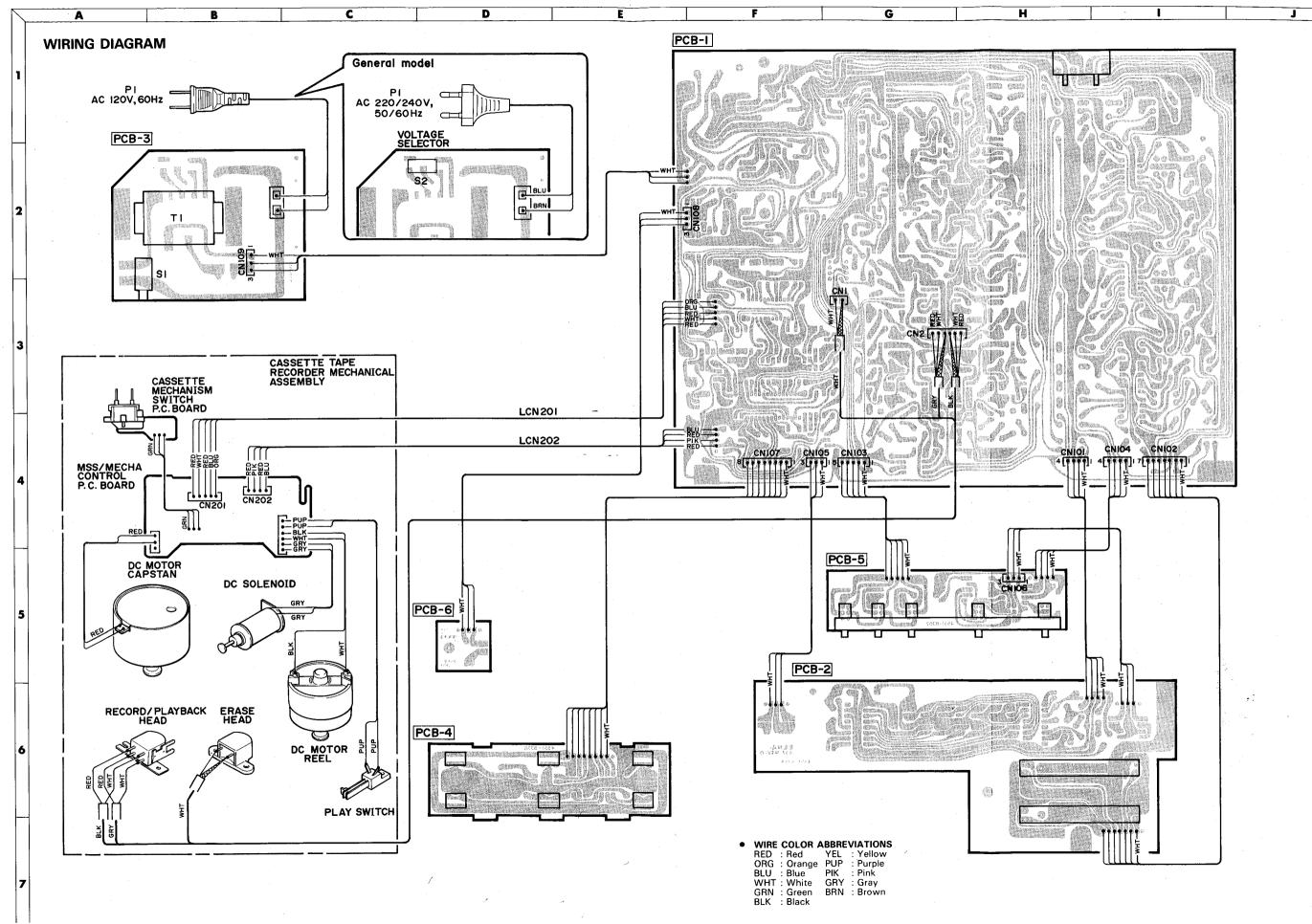
SPC: Special Capacitor ZD : Zener Diode

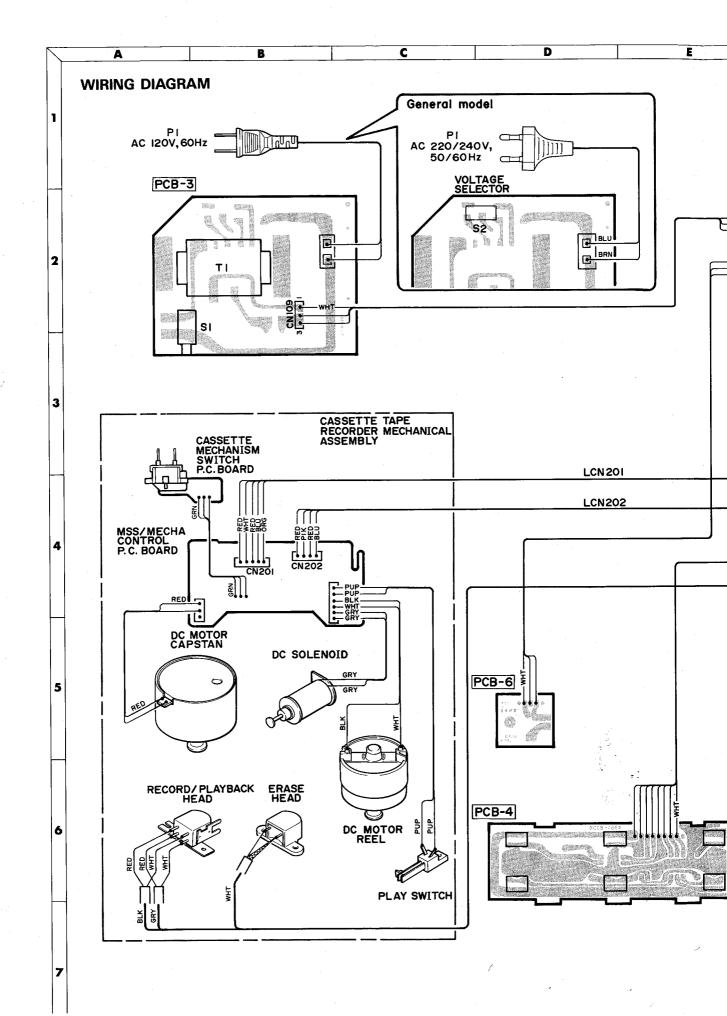
CLW: Connector with Lead Wire

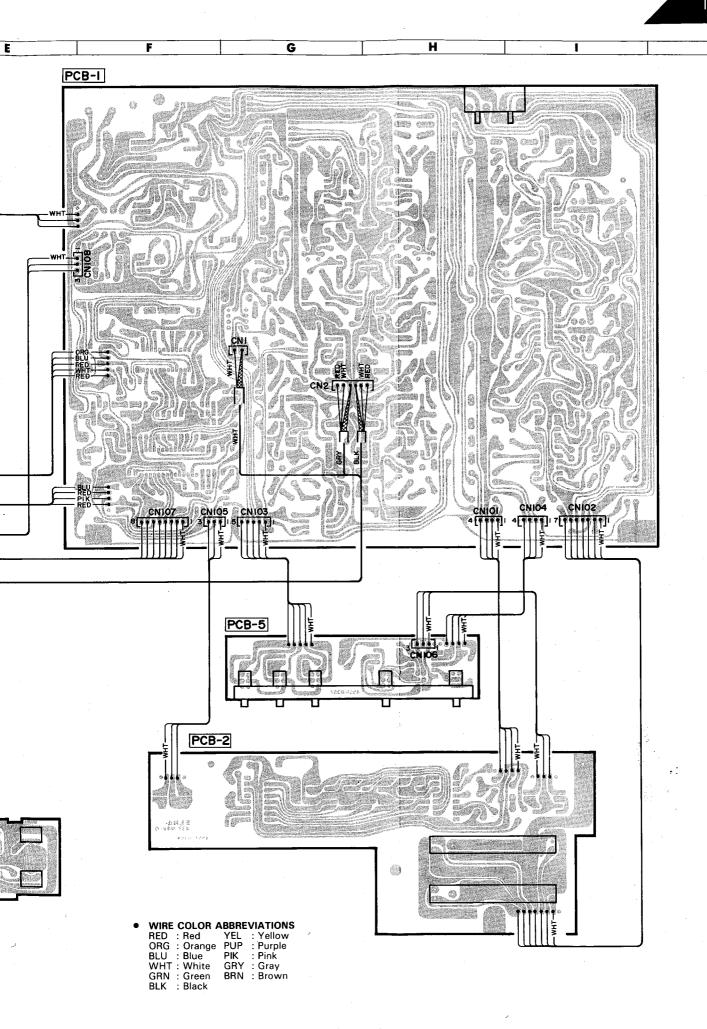
SAFETY RELATED COMPONENT. USE ONLY EXACT REPLACEMENT PART AS SPECIFIED.

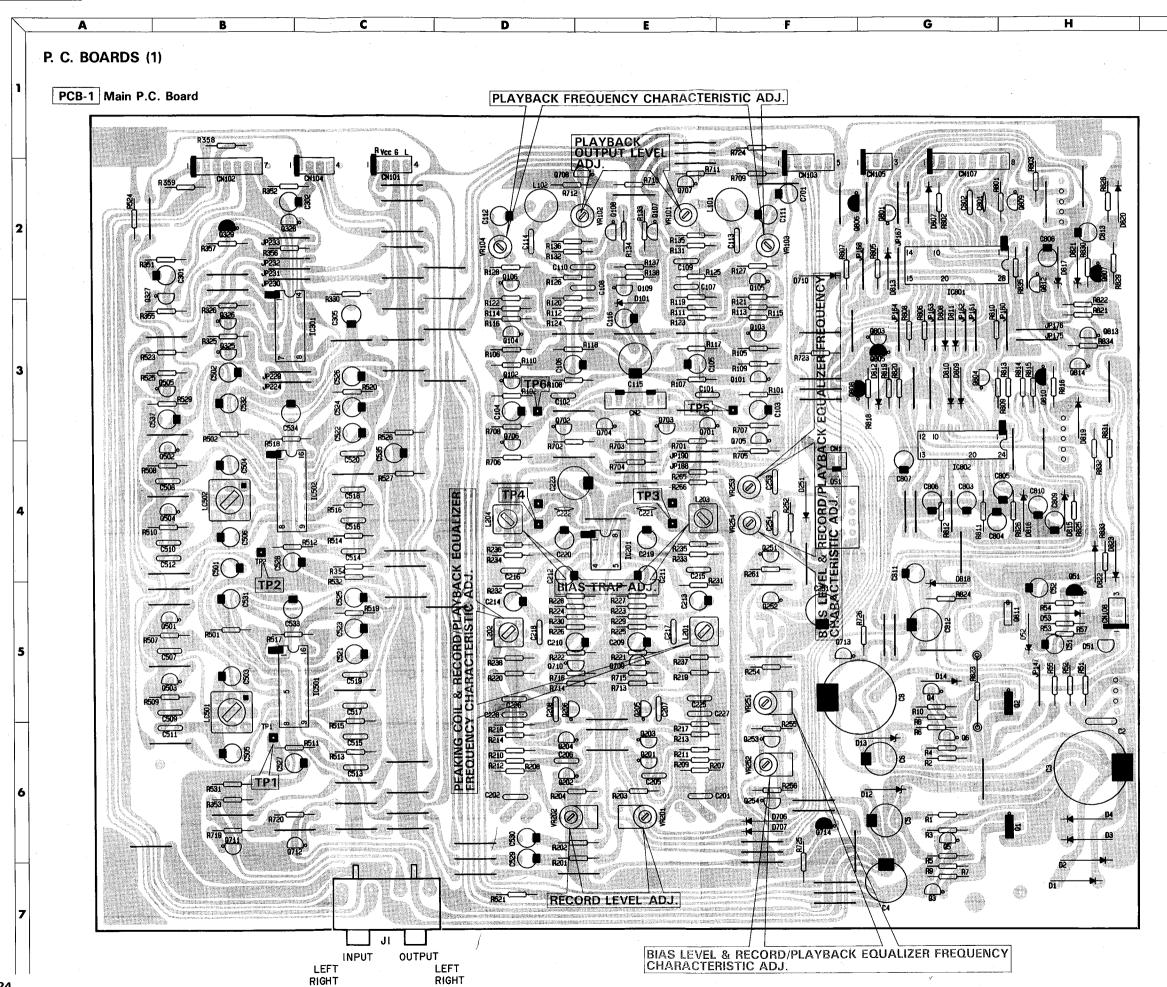
PIN CONNECTION DIAGRAM OF TRANSISTORS, DIODES AND ICS

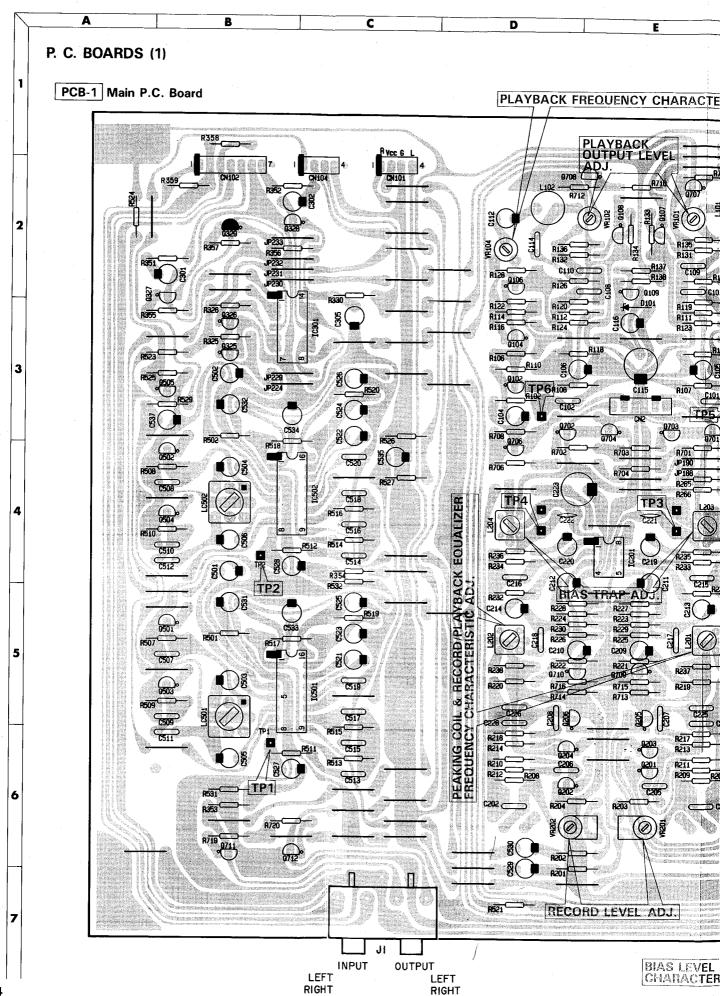


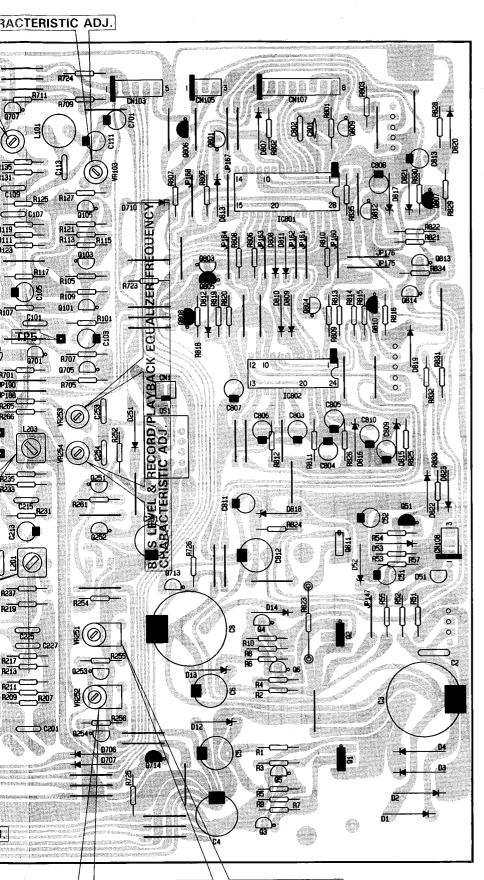




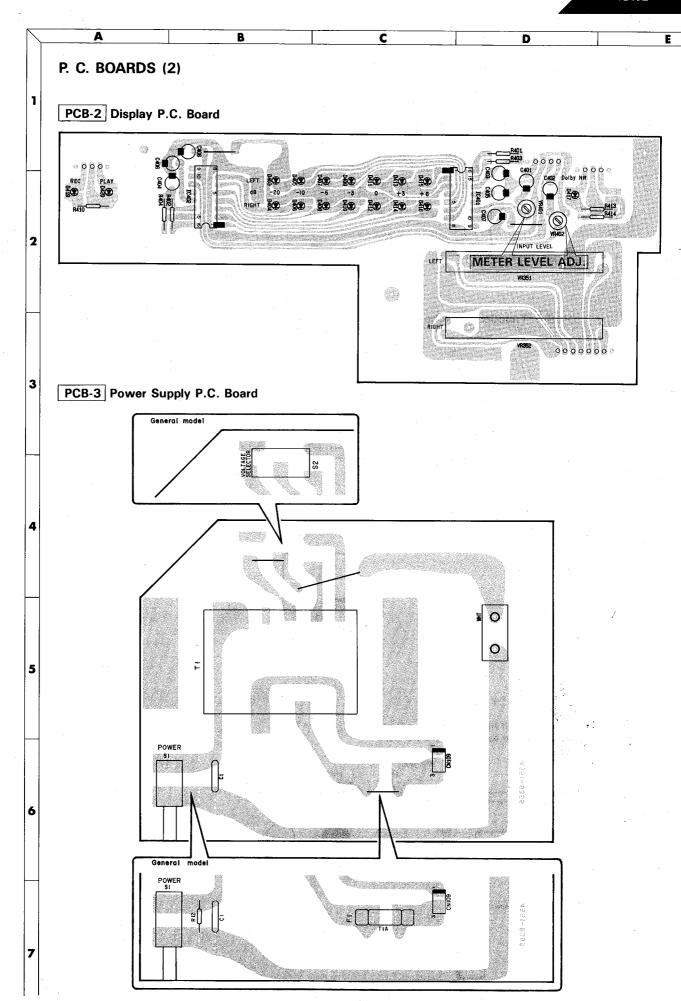


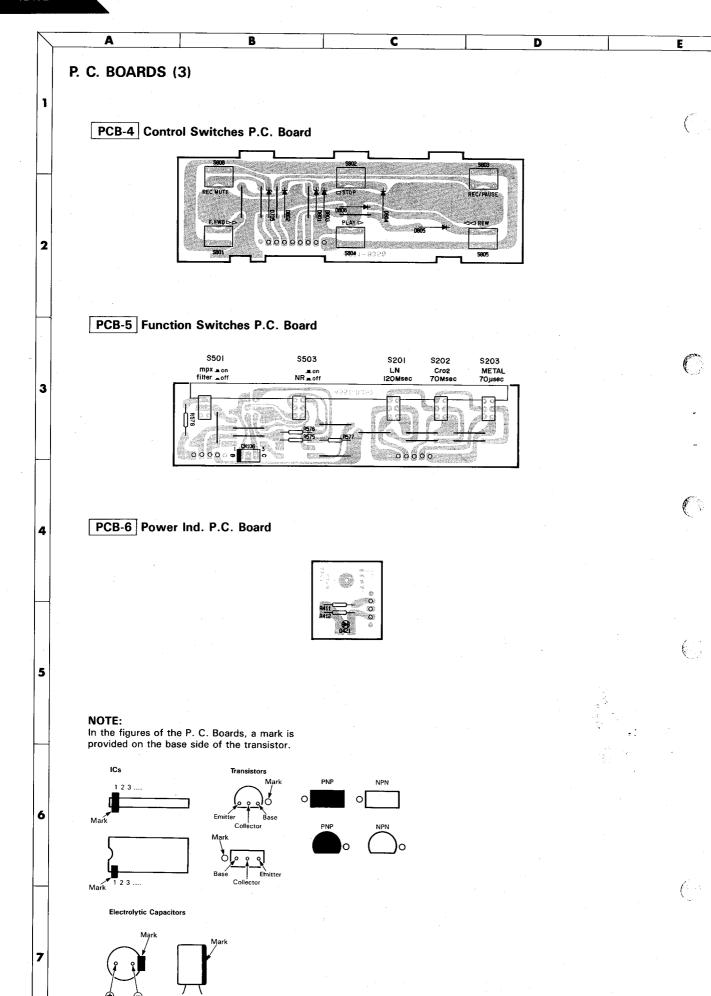




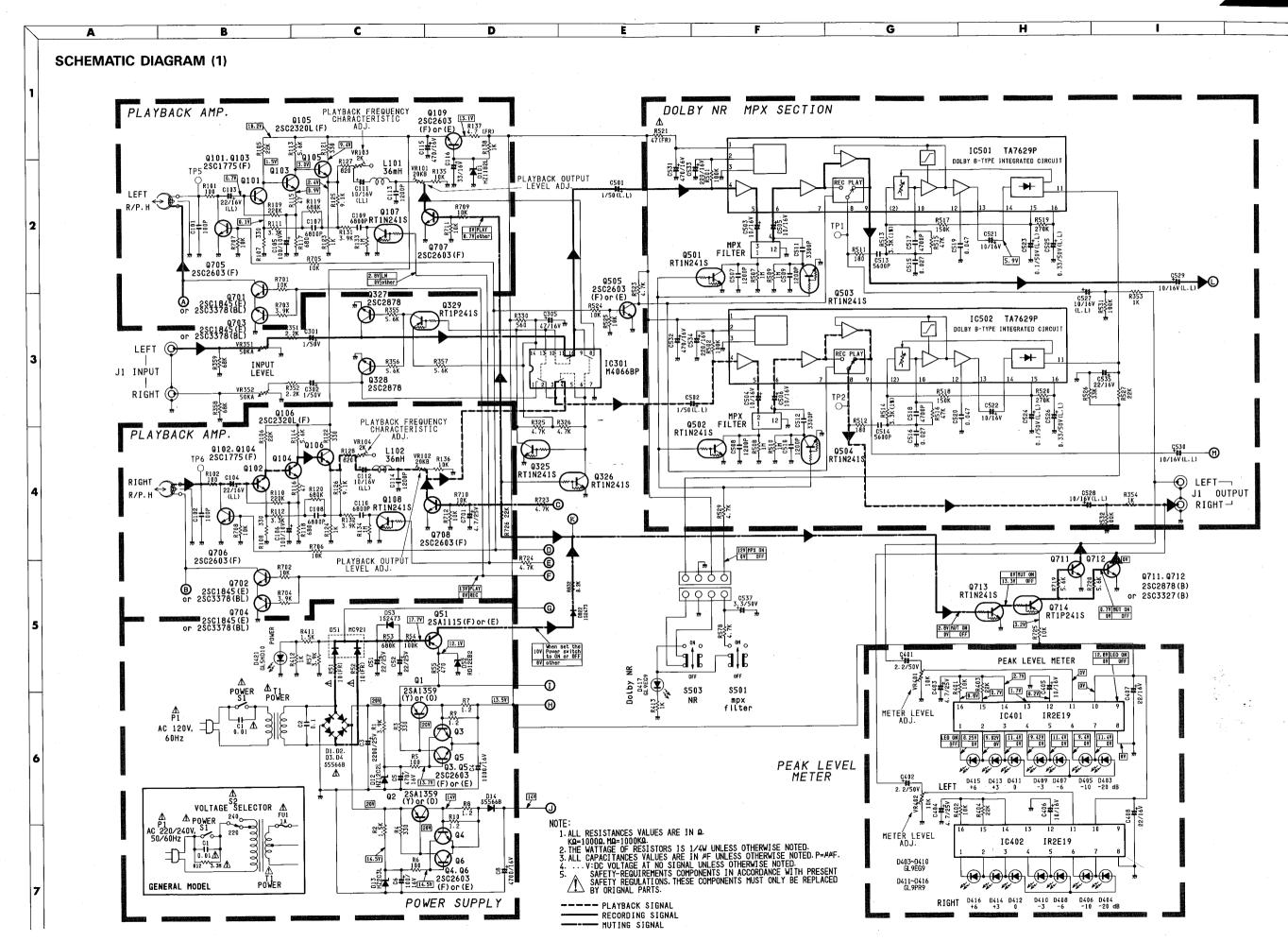


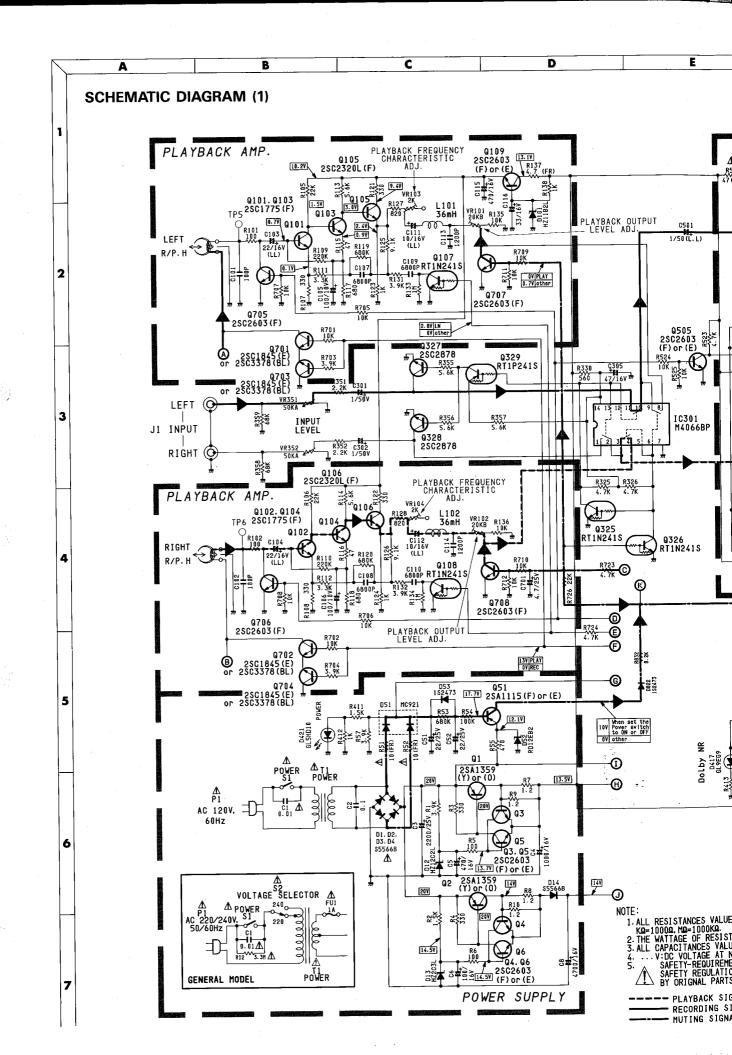
LEVEL & RECORD/PLAYBACK EQUALIZER FREQUENCY RACTERISTIC ADJ.

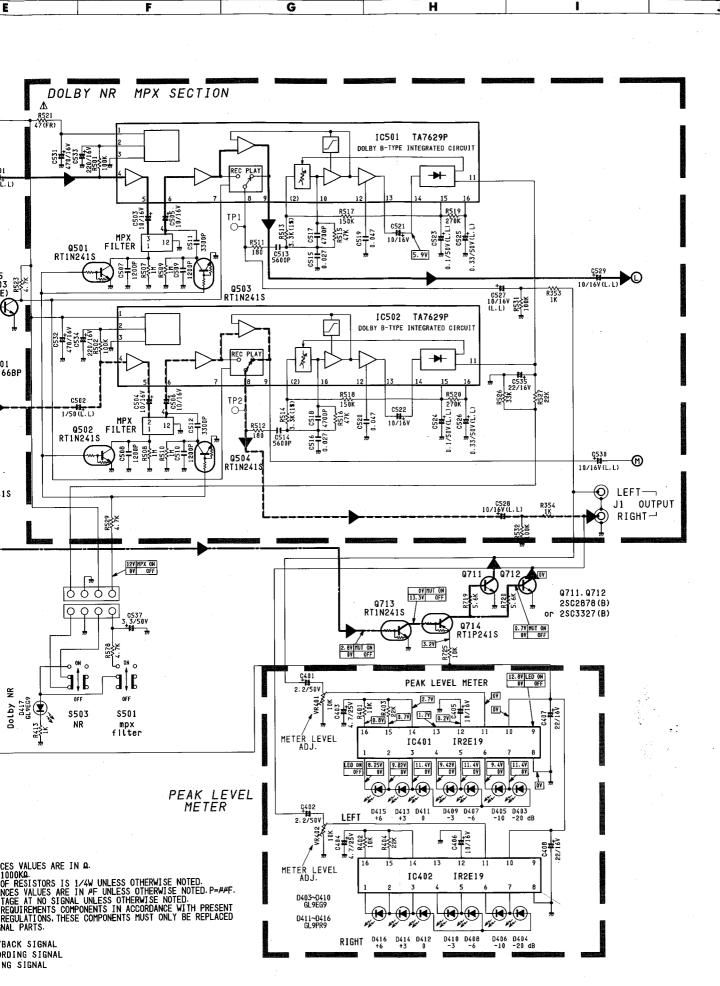


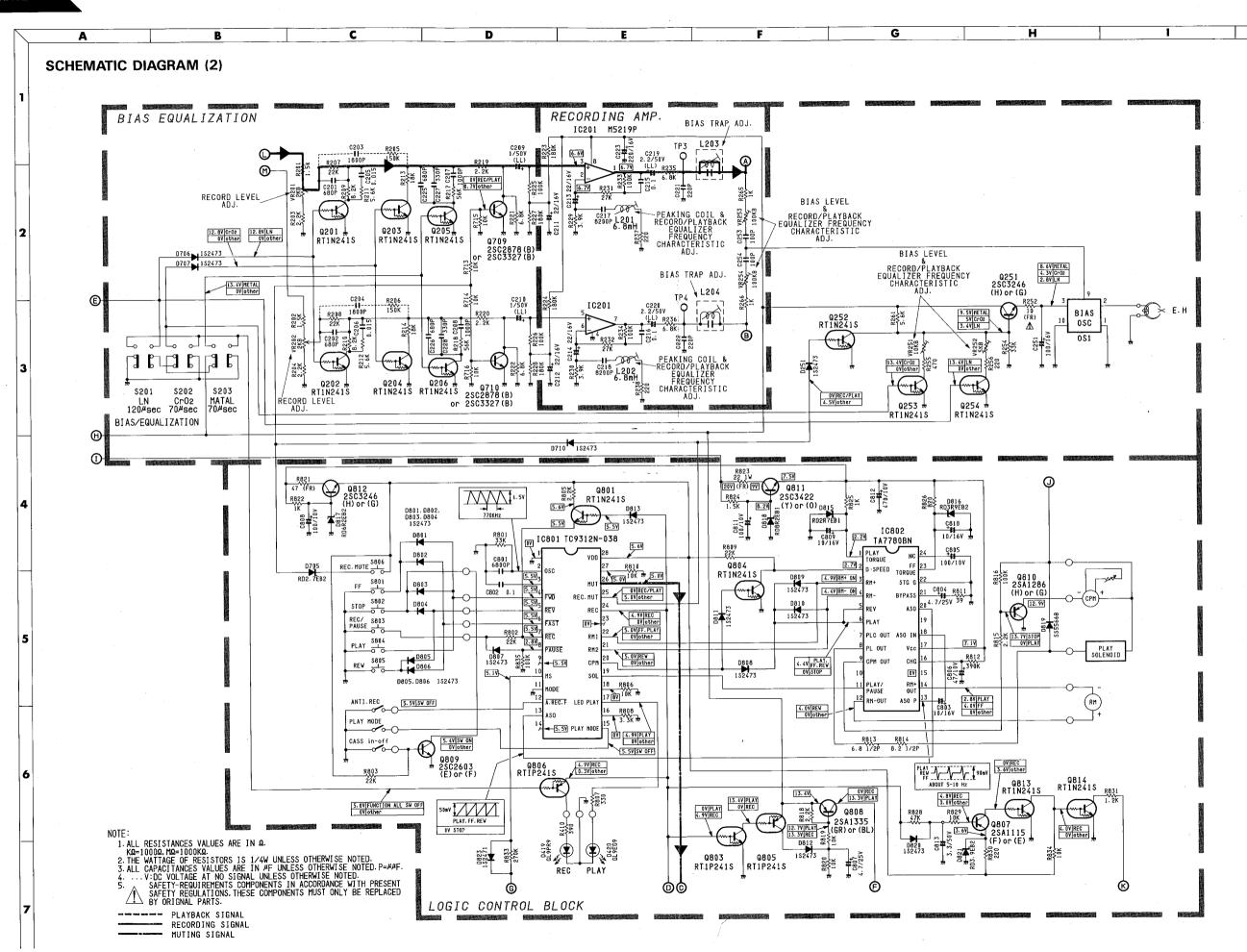


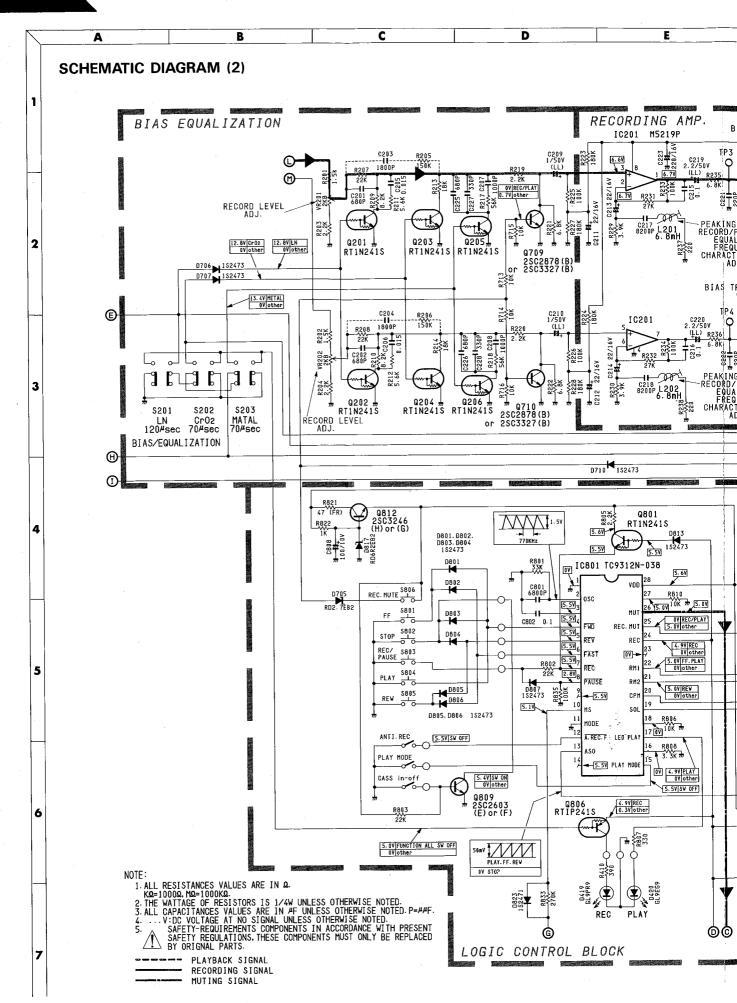
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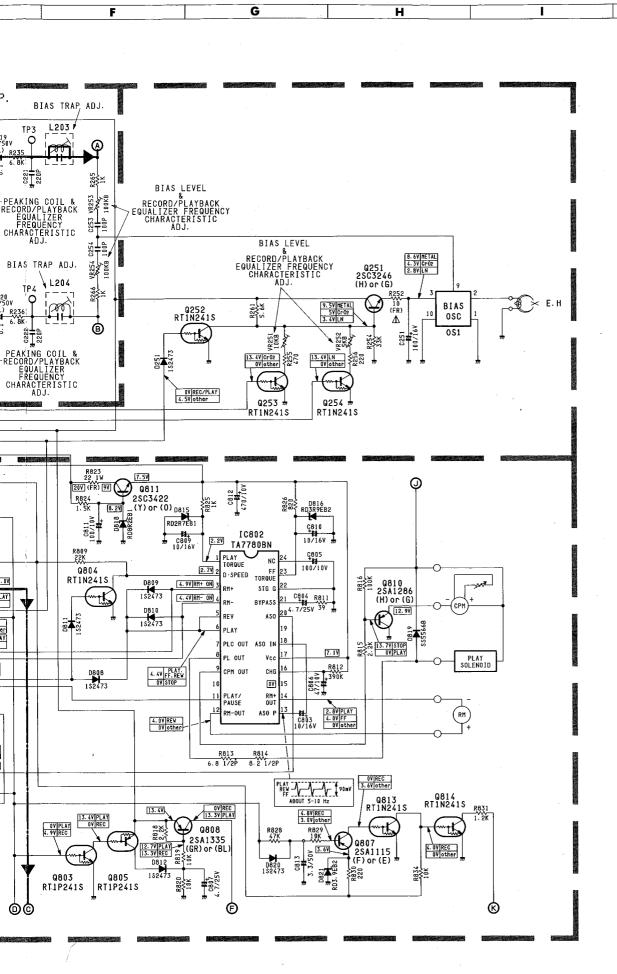




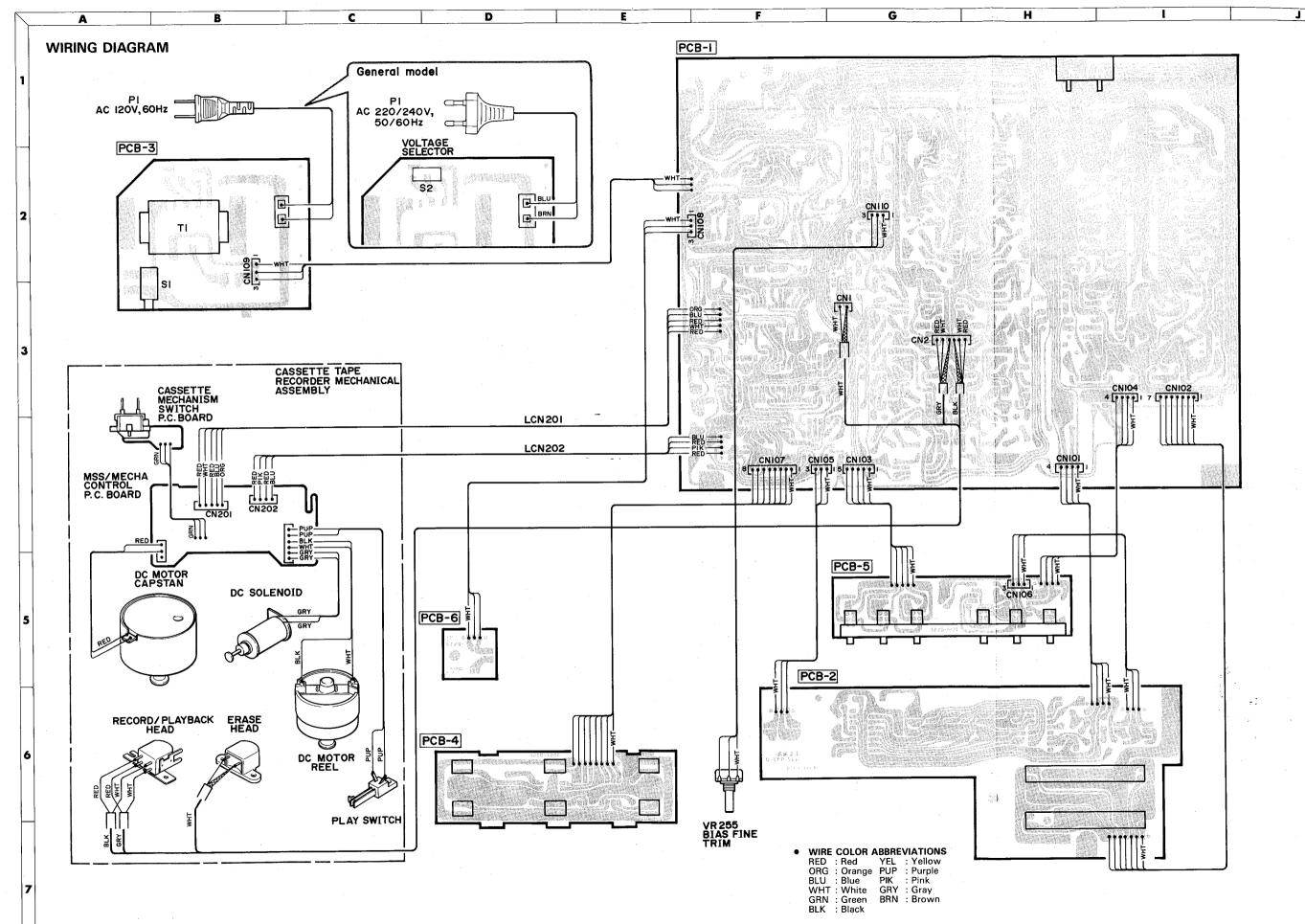


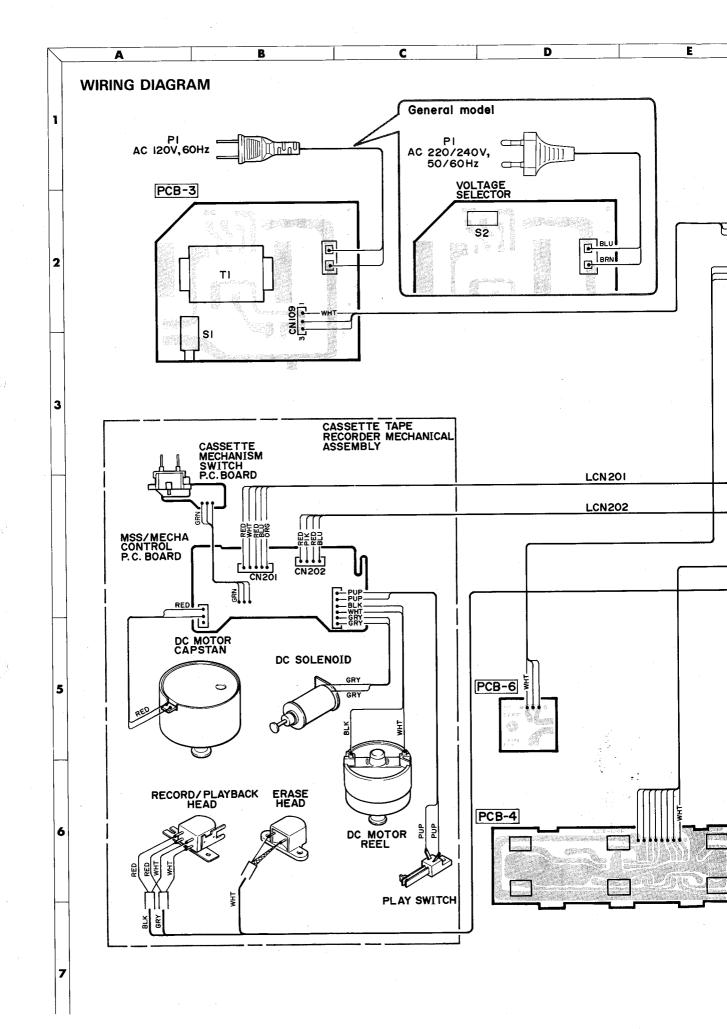


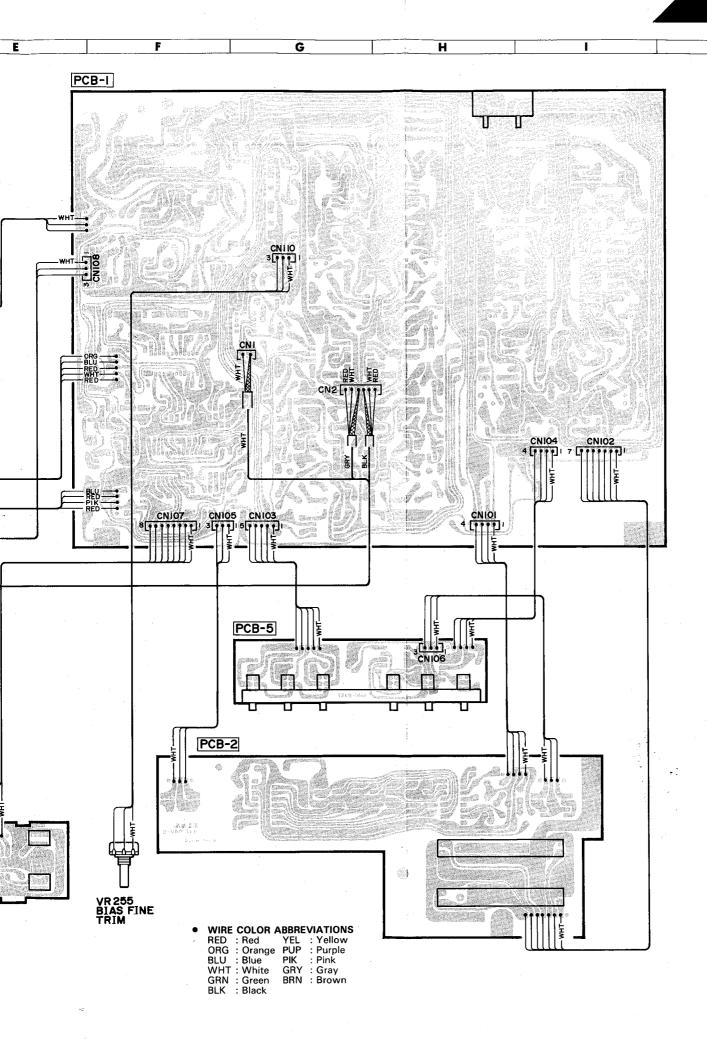


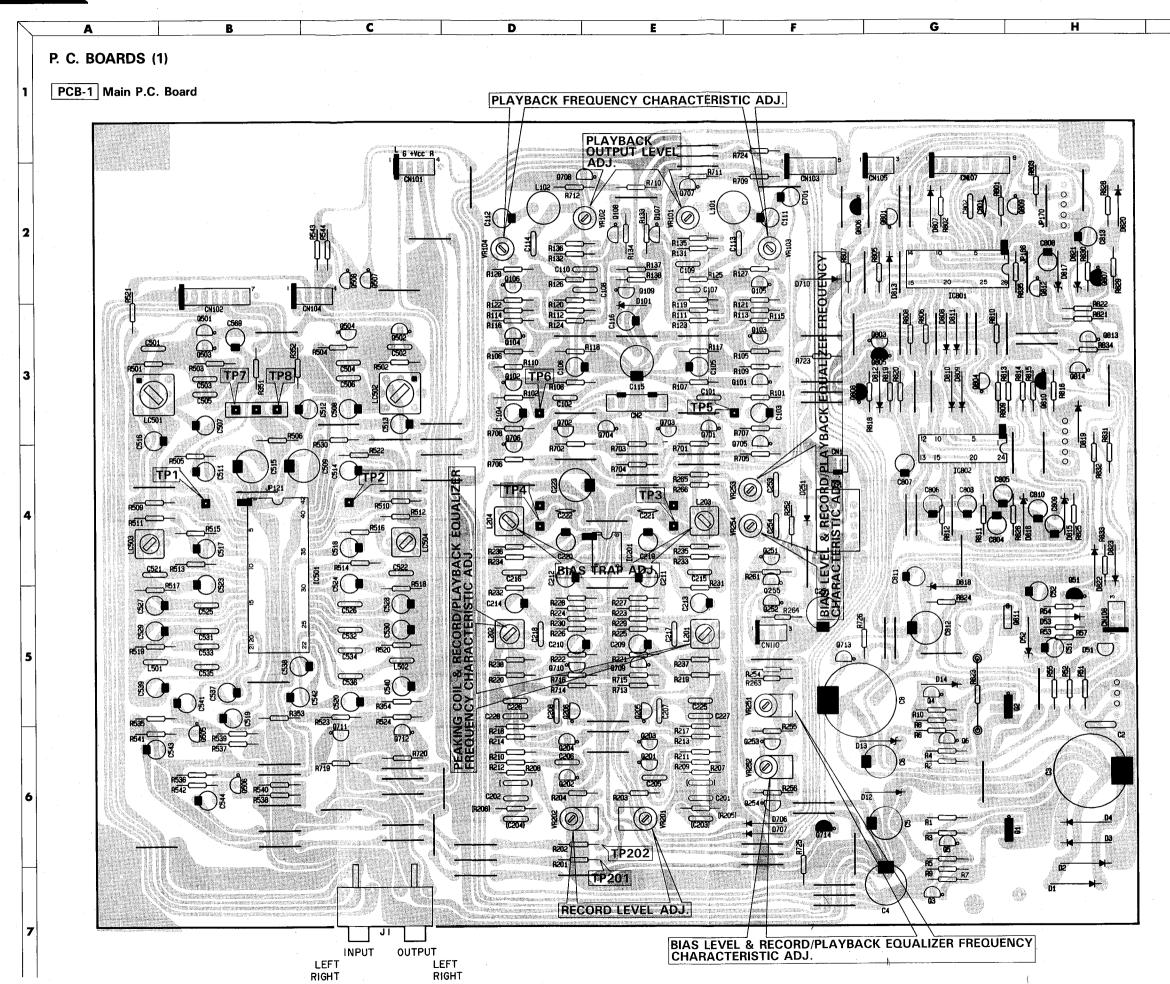


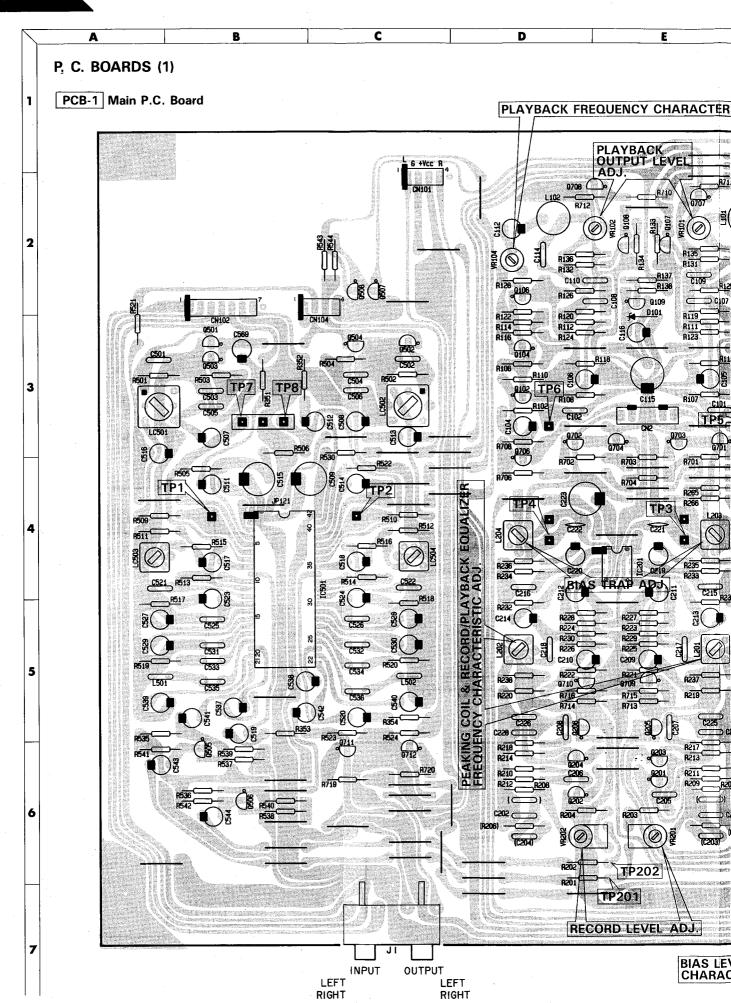
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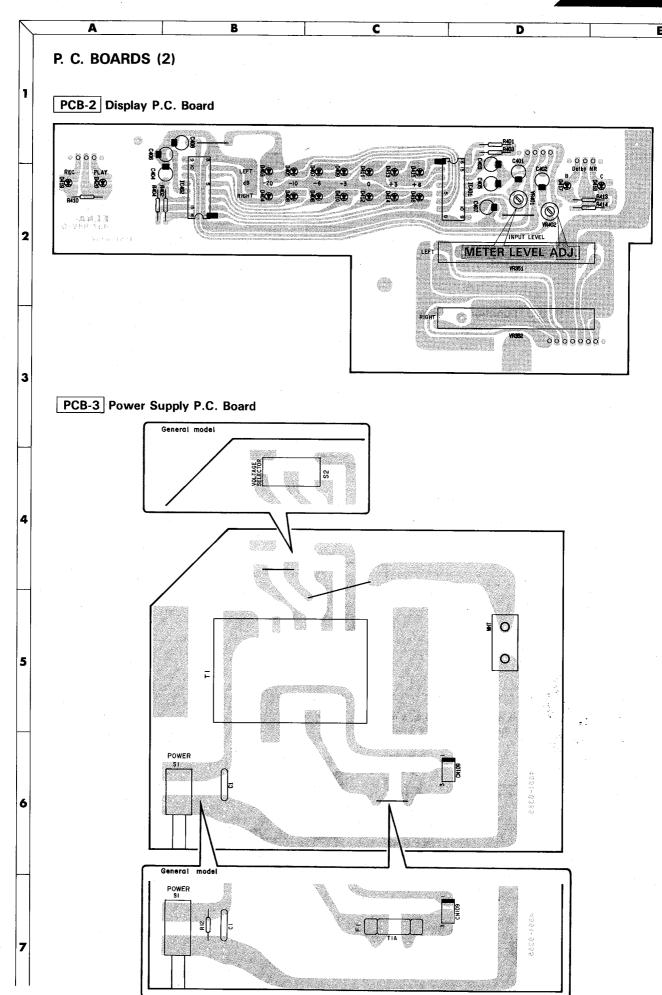


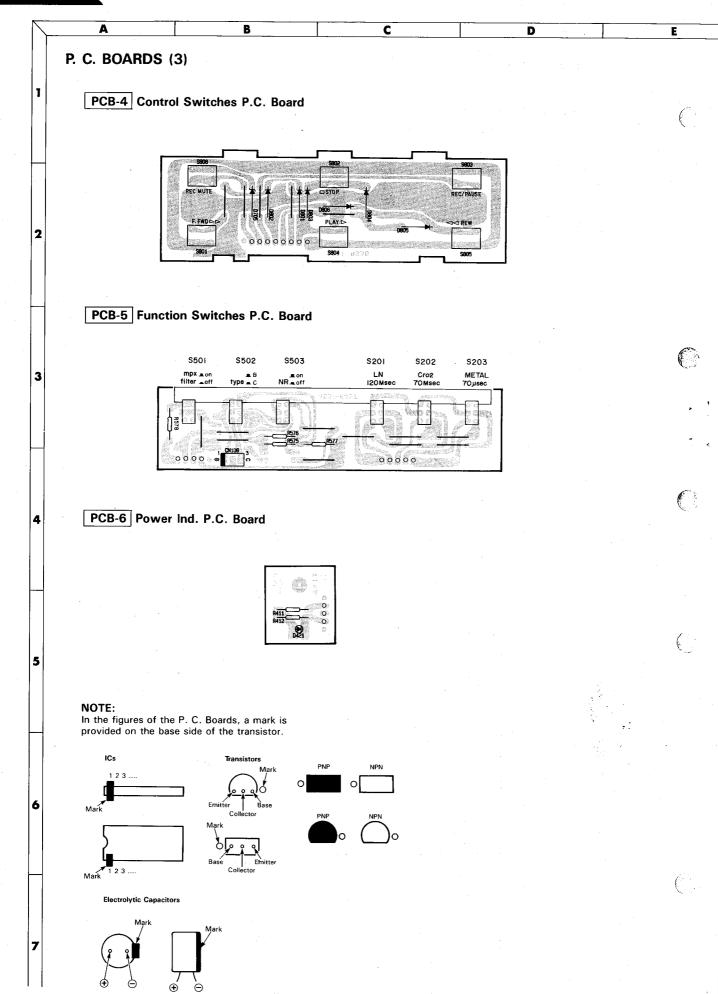




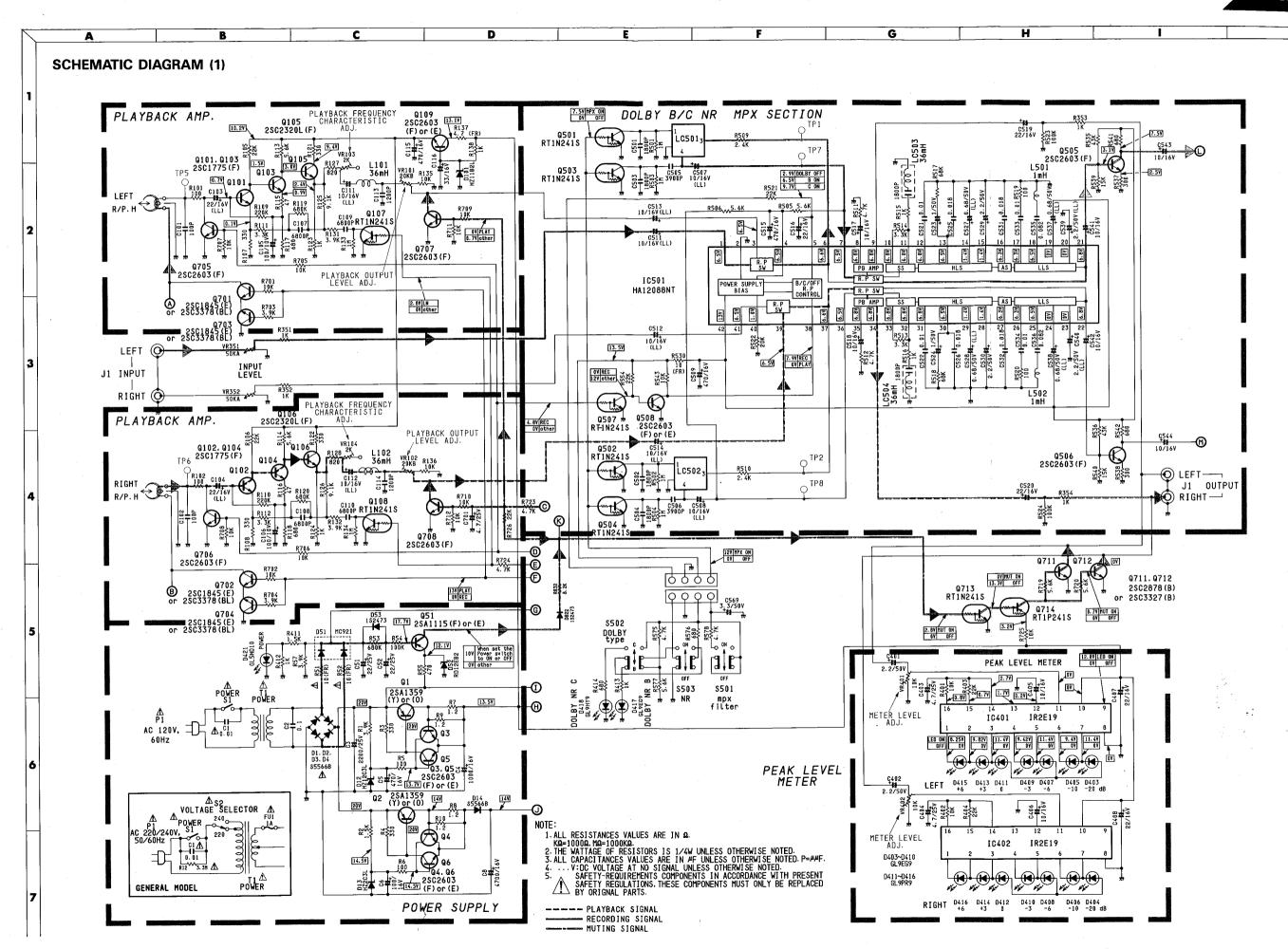
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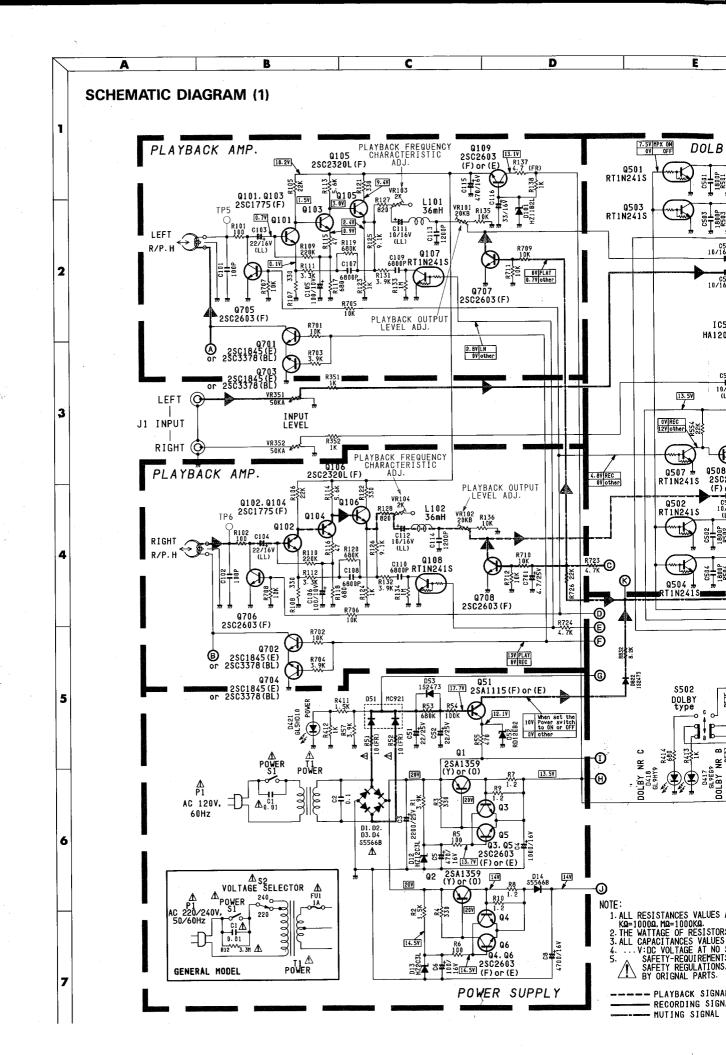
IAS LEVEL & RECORD/PLAYBACK EQUALIZER FREQUENCY HARACTERISTIC ADJ.

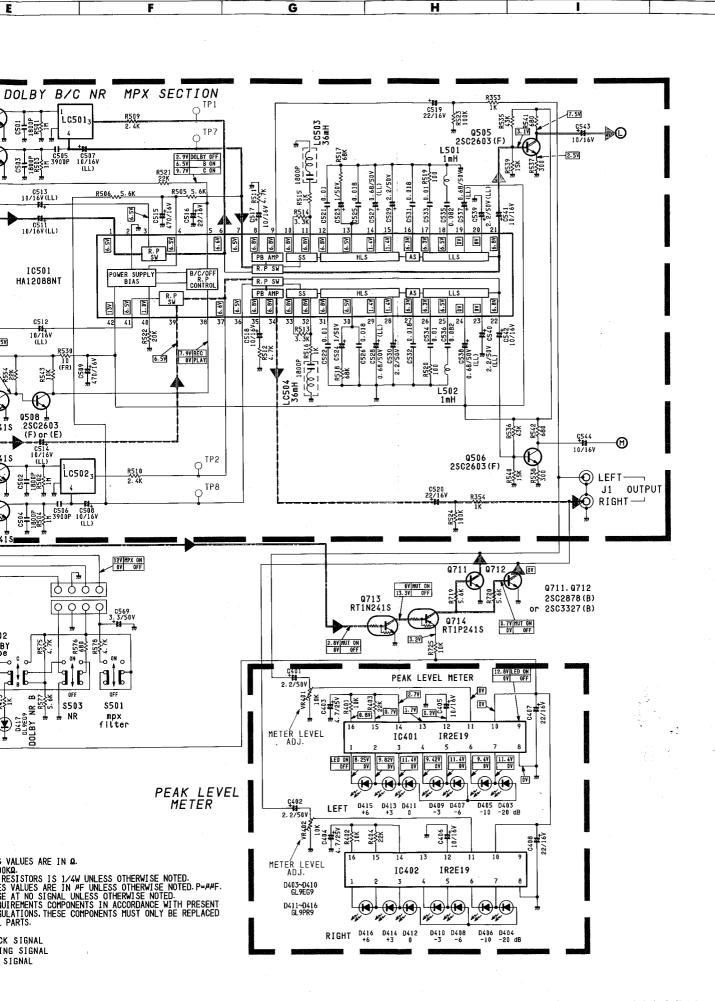


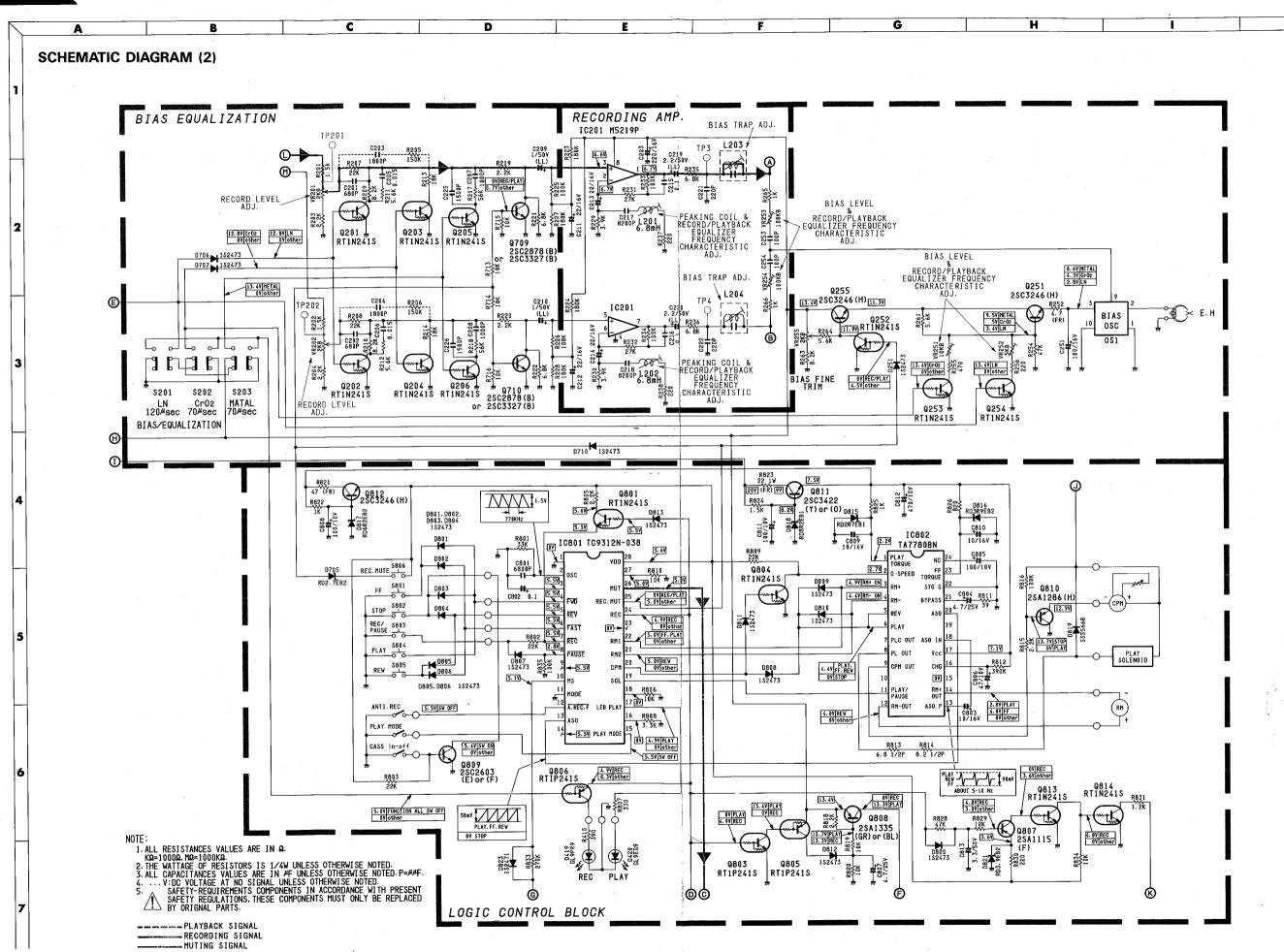


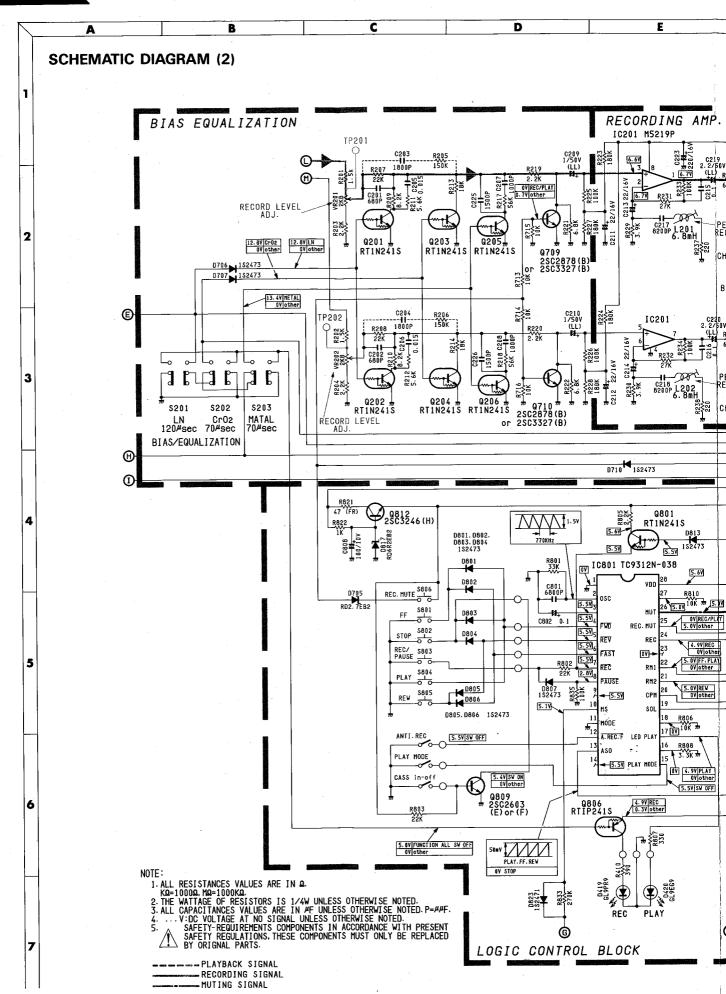
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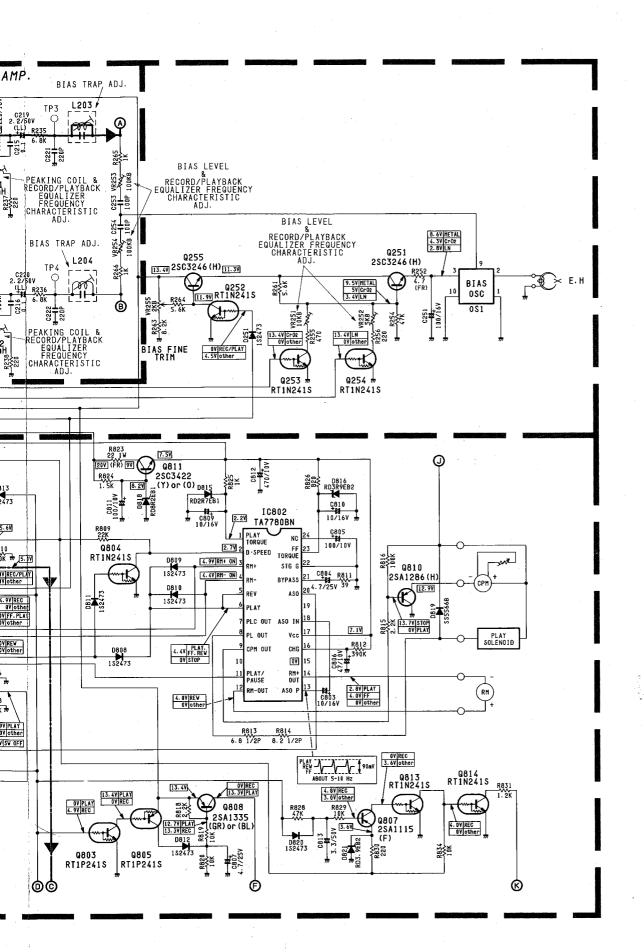












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